

Reduced encoder time complexity using enhanced adaptive multiple transform selection in versatile video coding

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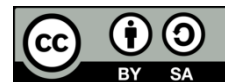
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ABSTRACT

When compressing a video, the architecture is provided with the width and height of the frame along with the video as input. The multiple transform selection (MTS) techniques are used to compress video. Traditional discrete cosine transforms (DCT) and other trigonometric (sine and cosine) transforms, such as DCT-8 and DST-7, are included in the pool of transforms. For the best outcomes, two additional transforms DST-2 and DCT-5 were also utilized to compress any remaining video frames. To improve coding effectiveness, peak signal-to-noise ratio (PSNR), and delay, 2D transforms are broken into 1D directional transforms for block sizes ranging from 4×4 to 64×64. The temporal complexity was greatly decreased by adding DST-2 to the existing video test model (VTM). The MTS's adaptive multiple transform (AMT) scheme already included a pool of transforms, but DST-2 was introduced as a fourth transform. Encoder time complexity was reduced by 22% on average without compromising PSNR and bitrate.

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1. INTRODUCTION

Compressing videos is the most demanding research interest mainly in consumer goods of electronics. ultra-high definition (UHD) should be supported in upcoming televisions. High definition (HD) must be supported by high-end smartphones. The trend of this nature continues for next years to come. Because of these efficient codecs will be in demand with video capacities more than high efficiency video coding (HEVC) [1]. Identical situation continuing the last 33 years. The new standard of coding whichever developed so far reduced bitrate to 50% compared to its previous versions maintaining quality peak signal-to-noise ratio (PSNR) [2].

Usage of orthogonal transforms in signal processing done in digital enhanced that led to recognizing patterns [3] and filtering using a wiener filter [4]. Noninvertible transformation enabled with orthogonal transforms that transform from pattern space to feature space with less dimensionality. Orthogonal transforms like walsh-hadamard, discrete-fourier, slant, and Haar were considered for different applications because they have fast computing algorithms [3]–[10] for them. Karhunen-loeve transform (KLT) is optimal with respect to the following: variance distribution [3], Estimation mean-square error criterion [4], [6] and rate-distortion function [6], lacks an algorithm that enables its fast computation [3]. It is here, DCT is introduced with the fast algorithm for its computation whose performance is closer to that of KLT compared to the performance of walsh hadamard transform, discrete fourier transform, and hadamard transform. Coding of high-resolution images done successfully by discrete cosine transform (DCT) [11]–[15]. DCT was implemented using double-size fast fourier transform (FFT) utilizing complex arithmetic conventionally due to the lack of an algorithm

which reduced its usage in many scenarios. A more efficient algorithm involving only real operations for computing fast DCT for a set of N points is described.

The algorithm can be prolonged to any number of $N=2^m$, $m \geq 2$. The method takes $\left(\frac{3N}{2}\right) \cdot (\log_2^{N-1}) + 2$ real summing operations and $(N \cdot \log_2^{N-\left(\frac{3N}{2}\right)} + 4)$ real product operations: approximately 6 times faster than the customary method using double-size FFT. Versatile video coding (VVC) will be based on transform, quantization, and entropy encoding. Multiple transform selection (MTS) schemes will be applied to versatile video coding also [16].

With MTS, two-D transforms will be computed by enacting first a one-D transform horizontal thenceforth one-D vertical transform. MTS scheme is useful to code both intra and inter-coded blocks [17]. MTS runs in all-intra, low-delay, and random-access configurations. VVC standard was unleashed as a new global standard in 2020 [18]. With VVC, forecasting error transformation is performed, quantized, and finally encoded as stream of bits. Following working draft 4 [19], the DCT-2 max. transform size is 64×64 , while the other two max. size is 32×32 .

Adaptive multiple transform (AMT) coding is effective at the cost of higher intricacy in computation on side of encoder. Joint video expert team (JVET) established joint exploration test model (JEM) software for testing gained extra coding methods and showed the importance of creating extra coding standards for video. Coding methods created in JEM allow a 30% increment in coding efficacy compared to HEVC [20], [21]. A modern approach namely AMT added by necessitating four extra transform varieties of the DCT/discrete sine transform (DST) family [22], [23]. In Inter coding contours, AMT coding efficiency is up by 7x compared to the reference of HEVC.

Modifications were made to the existing pool of transforms by including DST-2 to assess the time complexity of the video test model (VTM). The variations in encoding time ranged from around 19% to 28%. Efficient representation of energy in motion-compensated or residual images is crucial, and the 2-D DCT is widely utilized due to its ability to compactly represent energy in video compression applications.

Remnants of this paper are collocated as below. In section-2 deals with method followed by MTS of VVC. In section-3 discusses results of transform selection in MTS algorithm of VVC. In section 4 concludes the paper. MTS already contains DCT-2, DCT-8, and DST-7 as distinct transformations in it. Results were obtained by introducing DST-2 as the fourth transform.

2. METHOD

The flowchart in Figure 1 shows algorithm for the selection of transforms works as follows. The test video sequence is given as input. Video sequence may consist of three types of content. (i) smooth and continuous motion, (ii) complex and fast motion, (iii) discontinuous and sharp edges. If the video has smooth and continuous motion and $MTS_CU_flag=0$ then DCT-2 will be selected as a transform to compress the coding unit. If the video is having smooth and continuous motion and $MTS_CU_flag=1$ and $MTS_Hor_flag=1$ then DCT-8 is selected for horizontal and DST-2 is selected for vertical direction as transforms. If the video is having smooth and continuous motion and $MTS_CU_flag=1$ and $MTS_Hor_flag=0$ then DST-2 is selected for vertical and DST-7 is selected for horizontal direction as transforms.

Video may contain complex and fast motion. In this case, if $MTS_Hor_flag=0$ then DST-2 is selected for vertical and DST-7 is selected for horizontal direction as transforms. The content of the video may have discontinuities and sharp edges. If $MTS_Ver_flag=0$ then DCT-8 is selected for horizontal and DST-2 is selected for vertical direction as transforms. Test sequence video may have discontinuities and sharp edges. If $MTS_Ver_flag=1$ and $MTS_Hor_flag=0$ then DST-2 selected for vertical and DST-7 selected for horizontal direction as transforms. Video sequence may contain discontinuities and sharp edges. If $MTS_Ver_flag=1$ and $MTS_Hor_flag=1$ then DST-2 selected for vertical and DCT-8 selected for horizontal direction of transforms. Transforms selected are DST-2 and DCT-8 for this case. These transforms provide optimum compression to the selected block.

Video sequences are given as input to VTM's latest version for encoding without compromising the quality of video for evaluating PSNR, Bitrate, and encoding time. Transformation along with quantization performed before encoding. Multiple transforms are used to transform the frames of a given video sequence. DST-2 was added as the fourth transform to a pool of transforms existing in the AMT scheme of the MTS algorithm used in VVC. The average time difference after adding DST-2 varied from 19 % to 28.6% compared to the existing MTS.

Studies [24]–[27] in recent years reveal the usage of more than one transform allows the usage of different transforms for each block. This enhances the potential of the performance of the system for compressing videos. Many one-D arrangements emerge regularly in residuals of repaired motion and these arrangements are well characterized by group of one-D-DCTs (directional) [24]. Simulations in Biatex *et al.* [24]

manifest that rate-distortion attainment of H.264 enhances remarkably with the help of added 1D-DCTs. Figure 2 shows the concept of separable selection of transforms.

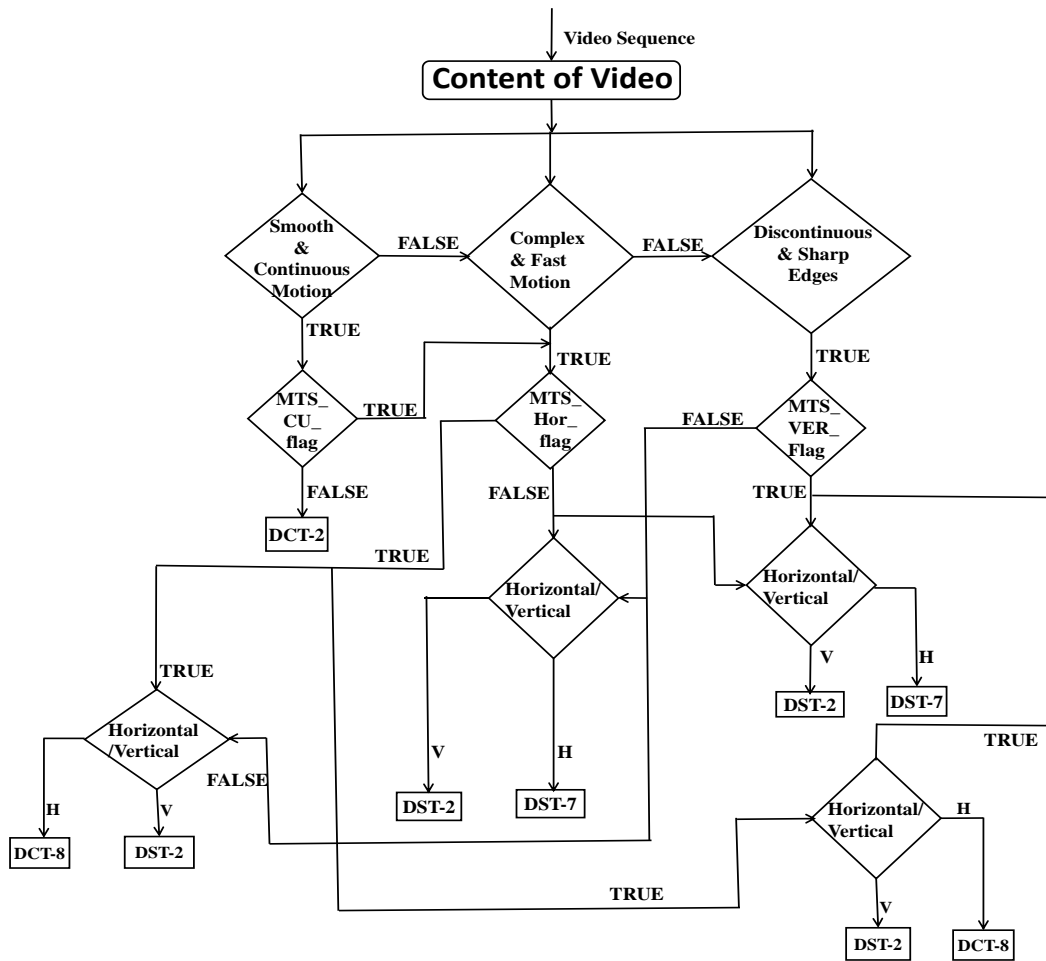


Figure 1. Flowchart for transform selection in MTS algorithm of VVC

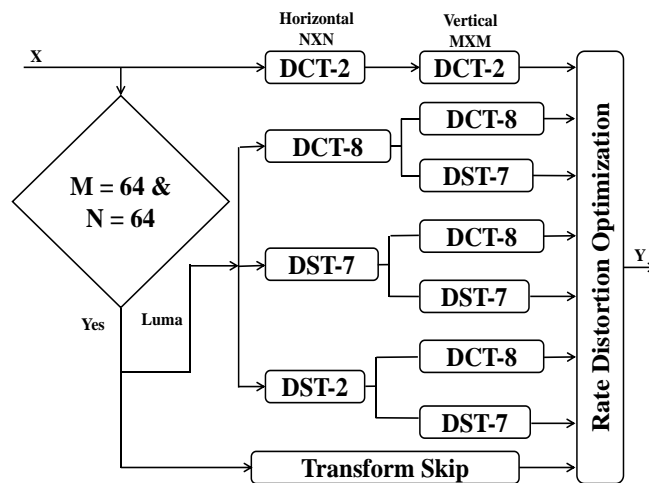


Figure 2. Two-D separable transform selection concept in VVC

In the JVET VVC Document, only horizontal and vertical one-D-DCTs are used instead of many directions. Simulations were performed with three settings: only two-D-DCT, two-D-DCT including all directional transforms, and only horizontal and vertical one-D-DCTs with 2D-DCT. For typical sequences of video, saving in number of bits is mostly because of the usage of horizontal along with vertical one-D-DCTs only. The selection of transforms based on the mode of transform is done in accordance with transform sets seen in Table 1.

Table 1. Pre-defined subsets of transform candidate

Transform set	Transform candidates
0	DST-7, DCT-8
1	DST-7, DST-2
2	DST-7, DCT-2

Perception of dissociable transforms has been broadly studied and scrutinized for HEVC [28], [29] which examines only DCT-2 with DST-7 for luma intra blocks with size 4×4 [30]. The separable transform concept was then integrated into JEM Software [31] which enables 5 trigonometric types of transforms. Five transform types include DCT-2, 5, and 8, and DST-2 and 7. Coding efficiency increased remarkably by around 3% of reduction in bit rate [31]. Gain in coding achieved at cost of large memory for saving coefficients of those transforms and overhead complexity for testing candidates of transform on the encoder side.

To conquer complexity of MTS JVET presented many contributions [32]–[35]. Solutions reduce complexity in no. of multiplication operations needed per pixel to apply DST-7 and DCT-8. Transform Approximation is not a surprise in literature and is thoroughly inspected for DCT-2 [36]–[45]. Chen *et al.* [36] gave an algorithm for approximation for 8-point DCT-2. This algorithm depends on factorizing matrix of DCT to even and odd and putting back odd with even for minimizing operation count still further. However, approximating DCT roughly of 8-point and utilizing for bigger sizes gives >5% loss in coding with respect to performance of rate-distortion.

Fast methods integrated to VTM-15.0. Experiments conducted are CTC test and QP low test. CTC tests include test condition in common given by JVET for experimenting proposals during VVC standard development. QP-quantization parameter set to 22, 27, 32, 37. Results provided both for inter MTS disabled and enabled. Outputs were provided with DST-2, DCT-8, DST-7, and DCT-2 in the existing AMT scheme in the MTS algorithm of VTM.

3. RESULTS AND DISCUSSION

Modifications were made to the existing pool of transforms in VVC standard to assess the time complexity. This involved disabling either DST-7 or DCT-8 individually to observe their impact on encoding time. Furthermore, the pool of transforms was expanded by appending DST-2. Throughout the evaluations, variations in the time taken to encode frames were observed, ranging from approximately 18% to 28%. To enhance the encoding capabilities of the VTM, the latest version incorporated a simulation with a fourth transform, DST-2, integrated into the MTS algorithm. This expanded the pool of transforms to include DCT-2, DCT-8, DST-7, and DST-2. MTS algorithm utilizes an AMT scheme. To comprehensively evaluate performance of the modified VTM, several parameters were considered, including PSNR, encoding time, and bitrate.

These parameters were assessed across various quantization parameter values for different video sequence categories (B1, B2, and B3). The evaluation also encompassed video sequences with rate of frames-24, 50 (fps) frames per second. Inclusion of DST-2 transforms in the MTS algorithm aimed to improve compression efficiency and overall video quality. By introducing this additional transform, the strike-a-balance between encoding time, achieved compression efficiency could be explored. Different quantization parameter values were examined to determine optimal configurations for specific video categories. The evaluation covered a diverse set of video sequences with varying frame rates, allowing for an in-depth analysis of the VTM's performance across different motion characteristics. This facilitated the identification of potential bottlenecks or areas for improvement in the encoding process. The modifications made to the pool of transforms, particularly the inclusion of DST-2, presented opportunities for optimizing the encoding process for specific video categories and frame rates. The evaluation of performance parameters such as PSNR, bitrate, and encoding time provided valuable insights into the trade-offs involved in achieving higher compression efficiency while maintaining satisfactory video quality. It is important to note that the results obtained from these evaluations are specific to the modifications made in the VTM and the chosen video sequences.

Further research and experimentation are required to validate these findings and explore additional aspects of the VVC standard's performance. Figure 3 shows RD-curves for B1-category of video sequences in existing (DCT-2, DCT-8, and DST-7) as well as modified (DCT-2, DCT-8, and DST-7 including DST-2). Figure 3 also shows RD-curves for the B2-category of video sequences in existing (DCT-2, DCT-8, and DST-7) as well as modified (DCT-2, DCT-8, and DST-7 including DST-2). Figure 3 also shows RD-Curves for the B3-category of video sequences in existing (DCT-2, DCT-8, and DST-7) as well as modified (DCT-2, DCT-8/DST-7 including DST-2). The average change in encoder time between the original and modified was calculated and shown in the Table 2.

Simulation results for different video sequences are shown in Table 2. Results show that PSNR and bitrate remained the same but encoding time got reduced by 28% (max). This result predicts a significant improvement in compression without compromising for quality of video by maintaining same PSNR and bitrate.

Table 2. Simulation results for different sequences

Class	Sequence	QP	$\Delta T\%$	BD-PSNR	BD-bitrate	
A (3,840×2,160)	Bosphorous	22	22.3254	0.0472	0.1010	
		27	23.0192	0.1906	0.0010	
		32	21.2345	0.1868	0.0090	
		37	19.7571	0.0170	0.0080	
	Jockey	22	20.7891	0.1743	0.0016	
		27	23.4586	0.0169	0.0045	
		32	26.5732	0.1826	0.0004	
		37	28.7957	0.1710	0.0008	
	B (1,920×1,080)	Kimono1	22	21.6365	0.0001	0.0000
			27	26.2344	0.0000	0.0000
			32	24.0456	0.0001	0.0000
			37	19.0980	0.0000	0.0001
Park scene		22	18.9654	0.0001	0.0000	
		27	19.5647	0.0000	0.0000	
		32	18.2345	0.0000	0.0001	
		37	20.9500	0.0000	0.0000	
Cactus		22	22.0147	0.0001	0.0000	
		27	18.8209	0.0000	0.0000	
		32	18.1236	0.0000	0.0000	
		37	22.2900	0.0001	0.0000	
C (832×480)	Race horses	22	20.1479	0.0000	0.0000	
		27	24.3432	0.0000	0.0000	
		32	18.3769	0.0000	0.0001	
		37	20.4123	0.0000	0.0000	
	BQ mall	22	21.7895	0.0000	0.0000	
		27	25.2077	0.0000	0.0000	
		32	22.3568	0.0000	0.0000	
		37	22.8070	0.0000	0.0000	
	D (416×240)	Race horses	22	24.5602	0.2305	0.0010
			27	22.1915	0.1725	0.0001
			32	21.0476	0.1073	0.0300
			37	21.3077	0.0850	0.0000
BQ square		22	27.0077	0.1985	0.0001	
		27	20.7731	0.0826	0.0001	
		32	23.6547	0.0357	0.0002	
		37	23.9852	0.0149	0.0030	
E (1,280×720)		Four people	22	22.5421	0.0297	0.0002
			27	21.4653	0.0062	0.0001
			32	20.5689	0.0073	0.0000
			37	20.3214	0.0165	0.0300
	Johnny	22	25.4492	0.0628	0.0001	
		27	24.0156	0.0262	0.0000	
		32	20.6086	0.0418	-0.0009	
		37	24.5687	0.0141	0.0004	
	Kirsten and Sara	22	25.7892	0.0551	0.0001	
		27	22.8082	0.0217	0.0000	
		32	28.1935	0.0152	0.0001	
		37	24.0356	-0.0334	0.0030	
Average		22.4222	0.0454	0.0040		

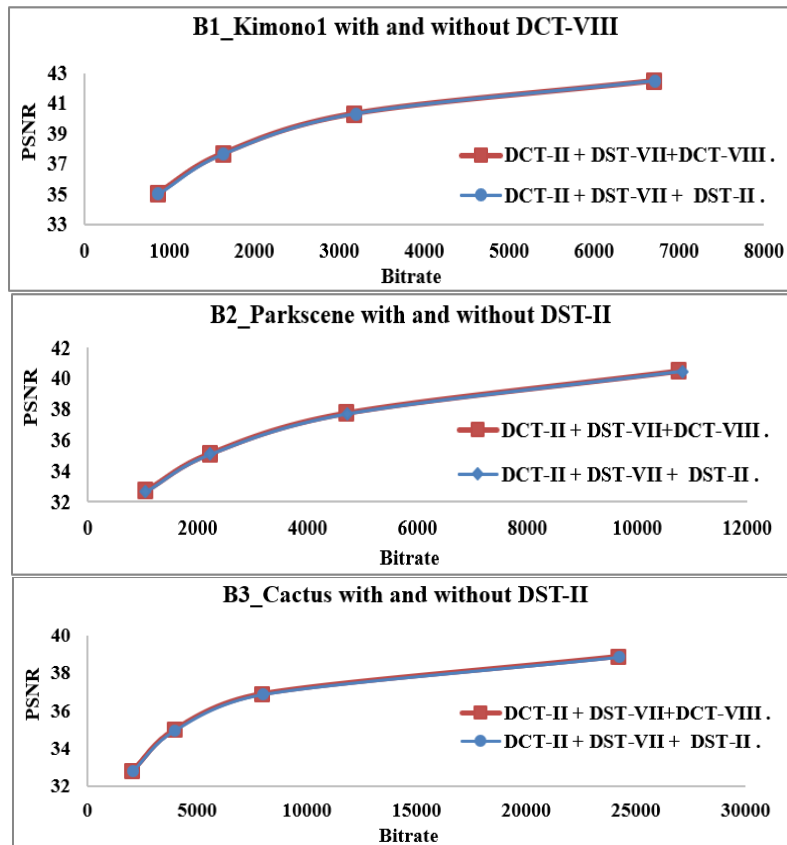


Figure 3. RD-curves of YUV test video sequences

4. CONCLUSION

Video sequences are given as input to VTM's latest version for encoding without compromising the quality of video for evaluating PSNR, Bitrate, and encoding time. Transformation along with quantization performed before encoding. Multiple transforms are used to transform the frames of a given video sequence. DST-2 was added as the fourth transform to a pool of transforms existing in the AMT scheme of the MTS algorithm used in VVC. The average time difference after adding DST-2 varied from 22.7% to 28.6% compared to the existing MTS.

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REFERENCES





- [1] S. S. Mardan and M. T. Hamood, "New fast Walsh–Hadamard–Hartley transform algorithm," *International Journal of Electrical and Computer Engineering*, vol. 13, no. 2, pp. 1533–1540, 2023, doi: 10.11591/ijece.v13i2.pp1533-1540.
- [2] S. Ben Jdidia, F. Belghith, M. Jridi, and N. Masmoudi, "A multicriteria optimization of the discrete sine transform for versatile video coding standard," *Signal, Image and Video Processing*, vol. 16, no. 2, pp. 329–337, Mar. 2022, doi: 10.1007/s11760-021-01925-2.
- [3] G. K. Shivanna and H. S. Prasantha, "Two-dimensional satellite image compression using compressive sensing," *International Journal of Electrical and Computer Engineering*, vol. 12, no. 1, pp. 311–319, 2022, doi: 10.11591/ijece.v12i1.pp311-319.
- [4] H. Fu *et al.*, "An extended context-based entropy hybrid modeling for image compression," *Signal Processing: Image Communication*, vol. 95, p. 116244, Jul. 2021, doi: 10.1016/j.image.2021.116244.
- [5] Z. Li *et al.*, "An optimized JPEG-XT-based algorithm for the lossy and lossless compression of 16-bit depth medical image," *Biomedical Signal Processing and Control*, vol. 64, p. 102306, Feb. 2021, doi: 10.1016/j.bspc.2020.102306.
- [6] Z. Zhang *et al.*, "Fast DST-VII/DCT-VIII with Dual Implementation Support for Versatile Video Coding," *IEEE Transactions on Circuits and Systems for Video Technology*, vol. 31, no. 1, pp. 355–371, Jan. 2021, doi: 10.1109/TCSVT.2020.2977118.
- [7] M. Cobrnac, A. Duspara, L. Dragic, I. Piljic, H. Mlinaric, and M. Kovac, "An area efficient and reusable HEVC 1D-DCT hardware accelerator," in *Lecture Notes in Computer Science (including subseries Lecture Notes in Artificial Intelligence and Lecture Notes in Bioinformatics)*, vol. 12043 LNCS, 2020, pp. 199–208.
- [8] M. Jridi, A. Alfalou, and P. K. Meher, "Efficient approximate core transform and its reconfigurable architectures for HEVC," *Journal of Real-Time Image Processing*, vol. 17, no. 2, pp. 329–339, Apr. 2020, doi: 10.1007/s11554-018-0768-x.

- [9] N. Li, Y. Zhang, and C.-C. J. Kuo, "Explainable machine learning based transform coding for high efficiency intra prediction," *arXiv*, 2020.
- [10] N. C. Bichwe and R. K. Chaurasiya, "Hardware Design of 8×8 and 16×16 2D Discrete Cosine Transform with N/2 Equations for Image Compression," in *Advances in Intelligent Systems and Computing*, vol. 1122 AISC, 2020, pp. 241–250.
- [11] S. R. Faraji and K. Bazargan, "Hybrid Binary-Unary Truncated Multiplication for DSP Applications on FPGAs," in *IEEE/ACM International Conference on Computer-Aided Design, Digest of Technical Papers, ICCAD*, Nov. 2020, vol. 2020-November, pp. 1–9, doi: 10.1145/3400302.3415700.
- [12] I. Farhat, W. Hamidouche, A. Grill, D. Menard, and O. Deforges, "Lightweight Hardware Implementation of VVC Transform Block for ASIC Decoder," in *ICASSP, IEEE International Conference on Acoustics, Speech and Signal Processing - Proceedings*, May 2020, vol. 2020-May, pp. 1663–1667, doi: 10.1109/ICASSP40776.2020.9054281.
- [13] N. Liyanage, C. Wijenayake, C. U. S. Edussooriya, A. Madanayake, R. Cintra, and E. Ambikairajah, "Low-complexity real-time light field compression using 4-D approximate DCT," in *Proceedings - IEEE International Symposium on Circuits and Systems*, Oct. 2020, vol. 2020-October, pp. 1–5, doi: 10.1109/iscas45731.2020.9180755.
- [14] H. K. A. Al-Azeez and N. N. Khamiss, "Optimal quality ultra high video streaming based H.265," *Indonesian Journal of Electrical Engineering and Computer Science*, vol. 18, no. 3, pp. 1474–1485, 2020, doi: 10.11591/ijeecs.v18.i3.pp1474-1485.
- [15] C. Araar, S. Ghanemi, M. Benmohammed, and H. Atoui, "Pruned improved eight-point approximate DCT for image encoding in visual sensor networks requiring only ten additions," *Journal of Real-Time Image Processing*, vol. 17, no. 5, pp. 1597–1608, Oct. 2020, doi: 10.1007/s11554-019-00918-2.
- [16] M. J. Garrido, F. Pescador, M. Chavarrias, P. J. Lobo, and C. Sanz, "A 2-D multiple transform processor for the versatile video coding standard," *IEEE Transactions on Consumer Electronics*, vol. 65, no. 3, pp. 274–283, Aug. 2019, doi: 10.1109/TCE.2019.2913327.
- [17] H. Sun, Z. Cheng, A. M. Gharehbaghi, S. Kimura, and M. Fujita, "Approximate DCT Design for Video Encoding Based on Novel Truncation Scheme," *IEEE Transactions on Circuits and Systems I: Regular Papers*, vol. 66, no. 4, pp. 1517–1530, Apr. 2019, doi: 10.1109/TCSI.2018.2882474.
- [18] A. Tissier, A. Mercat, T. Amestoy, W. Hamidouche, J. Vanne, and D. Menard, "Complexity Reduction Opportunities in the Future VVC Intra Encoder," in *IEEE 21st International Workshop on Multimedia Signal Processing, MMSP 2019*, Sep. 2019, pp. 1–6, doi: 10.1109/MMSP.2019.8901754.
- [19] Z. Zhang, X. Zhao, X. Li, Z. Li, and S. Liu, "Fast Adaptive Multiple Transform for Versatile Video Coding," in *Data Compression Conference Proceedings*, Mar. 2019, vol. 2019-March, pp. 63–72, doi: 10.1109/DCC.2019.00014.
- [20] A. Jain, N. Pandey, and P. Jain, "FPGA-based architecture for implementation of discrete sine transform," *Lecture Notes in Electrical Engineering*, vol. 509, 2019, pp. 13–22.
- [21] H. W. Ney, A. A. H. A. Rahman, A. H. Awab, M. S. Rusli, U. U. Sheikh, and G. K. Meng, "Hardware design of a scalable and fast 2-D hadamard transform for HEVC video encoder," *Indonesian Journal of Electrical Engineering and Computer Science*, vol. 15, no. 3, pp. 1401–1410, 2019, doi: 10.11591/ijeecs.v15.i3.pp1401-1410.
- [22] W. Hamidouche, P. Philippe, C. E. Mohamed, A. Kammoun, D. Menard, and O. Deforges, "Hardware-friendly DST-VII/DCT-VIII approximations for the Versatile Video Coding Standard," in *2019 Picture Coding Symposium, PCS 2019*, Nov. 2019, pp. 1–5, doi: 10.1109/PCS48520.2019.8954535.
- [23] "ISO/IEC CD 23090-3 Versatile Video coding, document N10692, Joint Video Experts Team (JVET) of ITU-T SG 16 WP3 and ISO/IEC JTC 1/SC 29/WG 11, 127th Meeting: Gothenburg," vol. July 2019, 2019. [Online]. Available: https://www.itu.int/wftp3/av-arch/jvet-site/2019_10_P_Geneva/JVET-P_Notes_d3.docx.
- [24] T. Biatek, V. Lorcy, and P. Philippe, "Transform Competition for Temporal Prediction in Video Coding," *IEEE Transactions on Circuits and Systems for Video Technology*, vol. 29, no. 3, pp. 815–826, Mar. 2019, doi: 10.1109/TCSVT.2018.2805877.
- [25] "Versatile Video coding (Draft 4), document JVET-M1001-v7, Joint Video Experts Team (JVET) of ITU-T SG 16 WP3 and ISO/IEC JTC 1/SC 29/WG 11, 13th Meeting, Marrakech," vol. Jan 2019, 2019. [Online]. Available: https://www.itu.int/wftp3/av-arch/jvet-site/2019_01_M_Marrakech/JVET-M_Notes_dB.docx.
- [26] "Algorithm Description for Versatile Video Coding Test Model 2 (VTM2), document JVET-K1002-v2, Joint Video Experts Team (JVET) of ITU-T SG 16 WP3 and ISO/IEC JTC 1/SC 29/WG 11, 12th Meeting: Macao, CN, 03–12 Oct. 2018" vol. Oct 2018, 2018. [Online]. Available: https://www.itu.int/wftp3/av-arch/jvet-site/2018_10_L_Macao/JVET-L_Notes_d1.docx.
- [27] H. A. F. Almurib, T. N. Kumar, and F. Lombardi, "Approximate DCT Image Compression Using Inexact Computing," *IEEE Transactions on Computers*, vol. 67, no. 2, pp. 149–159, Feb. 2018, doi: 10.1109/TC.2017.2731770.
- [28] A. Said, H. Egilmez, V. Seregin, M. Karczewicz, "Complexity Reduction for adaptive Multiple Transforms (AMTs) using Adjustment Stages," *document JVET-J0066 v3, San Diego, CA, USA*, no. Apr. 2018, 2018.
- [29] N. Sidaty, W. Hamidouche, O. Deforges, and P. Philippe, "Compression efficiency of the emerging video coding tools," in *Proceedings - International Conference on Image Processing, ICIP*, Sep. 2018, vol. 2017-September, pp. 2996–3000, doi: 10.1109/ICIP.2017.8296832.
- [30] A. Said, H. Egilmez, V. Seregin, M. Karczewicz, and V. Seregin, "Efficient implementations of amt with transform adjustment stages," in *Document JVET-K0272 11th JVET Meeting: Ljubljana*, SI. 2018.
- [31] P. Philippe and V. Lorcy, "Further Simplification for AMT Complexity Reduction," (CE6.1.2), *document JVET-K0299, Ljubljana, SI, USA*, no. Jul 2018, 2018.
- [32] "Further simplification for AMT complexity reduction (CE6.1.2) [P. Philippe (Orange), V. Lorcy (bcm)]. [Online]. Available: http://phenix.it-sudparis.eu/jvet/doc_end_user/documents/11_Ljubljana/wg11/JVET-K0299-v1.zip.
- [33] A. Kammoun, W. Hamidouche, F. Belghith, J. F. Nezan, and N. Masmoudi, "Hardware design and implementation of adaptive multiple transforms for the versatile video coding standard," *IEEE Transactions on Consumer Electronics*, vol. 64, no. 4, pp. 424–432, Nov. 2018, doi: 10.1109/TCE.2018.2875528.
- [34] B. Mohamed *et al.*, "High-level synthesis hardware implementation and verification of HEVC DCT on SoC-FPGA," in *ICENCO 2017 - 13th International Computer Engineering Conference: Boundless Smart Societies*, Dec. 2018, vol. 2018-January, pp. 361–365, doi: 10.1109/ICENCO.2017.8289815.
- [35] X. Zhao, J. Chen, M. Karczewicz, A. Said, and V. Seregin, "Joint Separable and Non-Separable Transforms for Next-Generation Video Coding," *IEEE Transactions on Image Processing*, vol. 27, no. 5, pp. 2514–2525, May 2018, doi: 10.1109/TIP.2018.2802202.
- [36] Z. Chen, Q. Han, and W. K. Cham, "Low-Complexity Order-64 Integer Cosine Transform Design and its Application in HEVC," *IEEE Transactions on Circuits and Systems for Video Technology*, vol. 28, no. 9, pp. 2407–2412, Sep. 2018, doi: 10.1109/TCSVT.2018.2822319.





- [37] J. R. Ohm and G. J. Sullivan, "Versatile video coding—towards the next generation of video compression." In *Picture Coding Symposium*, Vol. 2018. 2018.
- [38] M. Masera, M. Martina, and G. Masera, "Adaptive Approximated DCT Architectures for HEVC," *IEEE Transactions on Circuits and Systems for Video Technology*, vol. 27, no. 12, pp. 2714–2725, 2017, doi: 10.1109/TCSVT.2016.2595320.
- [39] J. Chen, E. Alshina, G. J. Sullivan, J.-R. R. Ohm, and J. Boyce, "Algorithm description of Joint Exploration Test Model 3," *Joint Video Exploration Team (JVET) of ITU-T SG16 WP3 and ISO/IEC JTC1/SC29/WG11*, pp. 1–30, 2016.
- [40] G. Renda, M. Masera, M. Martina, and G. Masera, "Approximate Arai DCT architecture for HEVC," in *Proceedings - 2017 1st New Generation of CAS, NGCAS 2017*, Sep. 2017, pp. 133–136, doi: 10.1109/NGCAS.2017.38.
- [41] M. Masera, M. Martina, and G. Masera, "Odd type DCT/DST for video coding: Relationships and low-complexity implementations," in *IEEE Workshop on Signal Processing Systems, SiPS: Design and Implementation*, Oct. 2017, vol. 2017-October, pp. 1–6, doi: 10.1109/SiPS.2017.8110009.
- [42] M. O. Martínez-Rach, P. P. Peral, O. M. López-Granado, and M. P. Malumbres, "Optimizing the image R/D coding performance by tuning quantization parameters," *Journal of Visual Communication and Image Representation*, vol. 49, pp. 274–282, Nov. 2017, doi: 10.1016/j.jvcir.2017.09.015.
- [43] M. Jridi and P. K. Meher, "Scalable approximate dct architectures for efficient hevc-compliant video coding," *IEEE Transactions on Circuits and Systems for Video Technology*, vol. 27, no. 8, pp. 1815–1825, Aug. 2017, doi: 10.1109/TCSVT.2016.2556578.
- [44] G. Fracastoro, S. M. Fosson, and E. Magli, "Steerable Discrete Cosine Transform," *IEEE Transactions on Image Processing*, vol. 26, no. 1, pp. 303–314, Jan. 2017, doi: 10.1109/TIP.2016.2623489.
- [45] A. Arrufat, P. Philippe, K. Reuzé, and O. Déforges, "Low complexity transform competition for HEVC," in *ICASSP, IEEE International Conference on Acoustics, Speech and Signal Processing - Proceedings*, Mar. 2016, vol. 2016-May, pp. 1461–1465, doi: 10.1109/ICASSP.2016.7471919.

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