

Multiloop low bandwidth communication-based power sharing control for microgrids

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ABSTRACT

In parallel-connected inverter-based microgrids, the reactive power sharing accuracy can not have satisfactory results effortlessly. Mismatch in feeder impedances of the parallel-connected inverter-based microgrids is a significant cause of inaccurate reactive power-sharing. In voltage source inverters (VSI) based microgrids, especially for the islanded mode of operation, the conventional centralized or decentralized control techniques are not much helpful to control the voltage deviations due to impedance mismatch. Mismatch of the feeder impedance is compensated by the addition of fixed virtual impedance. Whereas, the change in the virtual impedance is compensated by adaptive virtual impedance-based control techniques which are helpful to mitigate power-sharing errors, but in most of the control schemes virtual impedance-based control mechanism needs pre-knowledge of feeder impedance which increases the computational burden. This paper presents a decentralized virtual impedance-based power sharing control. In the proposed control solution to mitigate reactive power sharing errors in distributed generation (DG) units, mismatch of the parallel-connected feeder impedance is equalized by regulating the addition of equivalent impedance to each DG inverter. Proposed control technique offers an independent implementation without any pre-knowledge of the feeder impedance. Hence, the implementation of the control scheme is a straightforward and computational burden is also reduced. Simulation results show the effectiveness of the control scheme.

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1. INTRODUCTION

Over the past few years distributed generation (DG), like renewable energy resources (RESs) has been widely considered in all over the world responding to rising demand for electricity and Global warming [1]. Subsequently, the implementation and operation of power electronics-based DGs connecting in parallel have many issues related to control, management and stability. DGs unit/units with connected loads and some control is referred to as microgrid system (MGs). MGs typically operates in grid-connected mode but can perform within a separate mode, i.e. the islanded mode which may be intentionally or in case of any fault

in the grid during operation. The switching from one mode to another mode could be manual or autonomous [2]. In case of automatic islanding, the power-sharing accuracy especially the reactive power sharing accuracy, voltage and frequency adjustments are achieved by improved droop-based control schemes which are decentralized and communication free control scheme [3-5]. Droop control takes the local measurements, and it can be implemented very easily, but the mismatch in the feeder's impedance of the parallel-connected DG inverters cause reactive power sharing errors and voltage deviations. When different load conditions are applied to the same network, the reactive power and voltage deviations increase significantly even for the local loads [6, 7]. Online estimator method has been implemented in [8], which estimates the voltage drop in transmission lines and voltage is then compensated to improve reactive power-sharing. However, this online estimation control scheme is applicable to the grid-connected mode of operation. If the microgrid is in islanded mode of operation, the online estimator control scheme only be implemented when it is switched to the grid-connected mode. Voltage compensation technique is used in [9] to mitigate the power-sharing inaccuracy, but the reactive power is not accurately shared, and also the stability of the system is not guaranteed. Equivalent feeder concept with the addition of virtual impedance to cover up the mismatch of the line impedance is employed in [10]. Another solution that does not depends on feeder impedance and data exchange presented in [11], which is followed by [12] and tested for various load changing conditions. The results showed that power-sharing is effectively done. In [13], virtual capacitor algorithms are introduced to show accuracy in power-sharing, but the scheme does not support the load varying conditions. Dead time concept presented in [14] for power-sharing, but the control scheme is very complicated and difficult to implement.

As shown in [15, 16] the virtual synchronous generator droop-based control techniques suggested for voltage source inverters (VSIs). Equal and accurate power-sharing among VSIs with parallel-connected DG inverter-based MG system in a decentralized manner has been discussed in detail. However, unfortunately, there were reactive power sharing errors caused by the mismatch of grid impedance recorded in the simulation results. At fundamental and harmonic frequencies solution for reactive power sharing errors presented in [17-19], where the VSI impedances are reshaped by adding the virtual impedance loop to avoid the mismatch impedance. The virtual impedance loop had been designed that is dependent on values of the feeder's physical impedance which can never be accurate and in case of adaptive virtual impedance the computational burden is the drawback of the control schemes. Reactive power-sharing improvement control strategy with the capacitive or inductive droop with the loop of virtual impedance implemented in [20-23], where the point of common coupling information and the line impedance measurements are much complicated and reactive power sharing errors are not removed up to the desired level. Moreover, the abrupt change in loads and plug and play feature of the microgrid system does not afford any fixed configuration. Here a proposed solution with adaptive virtual impedance-based control which calculates the real-time values of impedance and reshapes them accordingly is presented. The control mechanism is applicable to the islanded microgrids. Reactive power-sharing accuracy and voltage frequency errors are eliminated in the proposed control technique, which is easy to implement. The communication-link is used for exchanging the information between the central controller to the distributed generation units and vice versa. Microgrid central controller (MGCC) gets the information of the feeder impedance and modify the virtual impedance values accordingly to compensating the impedance mismatch. Paper is arranged as the introduction in section 1, operation of a parallel-connected inverter-based microgrid is given in Section 2. virtual impedance Based control, Proposed Adaptive Virtual Impedance multiloop control, simulation results and Conclusion are given in Section 3, 4, 5 and 6 respectively.

2. OPERATION OF PARALLEL CONNECTED INVERTER-BASED MICROGRID

2.1. Micro grid structure

In the microgrid under consideration here, there are two DG units; each unit is connected with the LC filter and local loads and load at point of common coupling (PCC). As shown in Figure 1, the central controller and main controller MGCC continuously monitor the microgrid operation and mode of operation, whether it is working under islanded or grid-connected mode. The information between MGCC and the DG units are carried out through the communication link that utilizes a very low bandwidth communication. A static transfer switch (STS) is inserted between the DG inverters and the main grid and PCC. Power-sharing errors are rectified automatically by the main grid in case of grid-connected mode. In an islanded mode of operation with parallel-connected inverters, the reactive power-sharing is a big concern in parallel connected inverter-based microgrids. So, the voltage and frequency deviations are the main issues when the microgrid is transferred to the islanded mode of operation as well as the abrupt change in the load conditions.

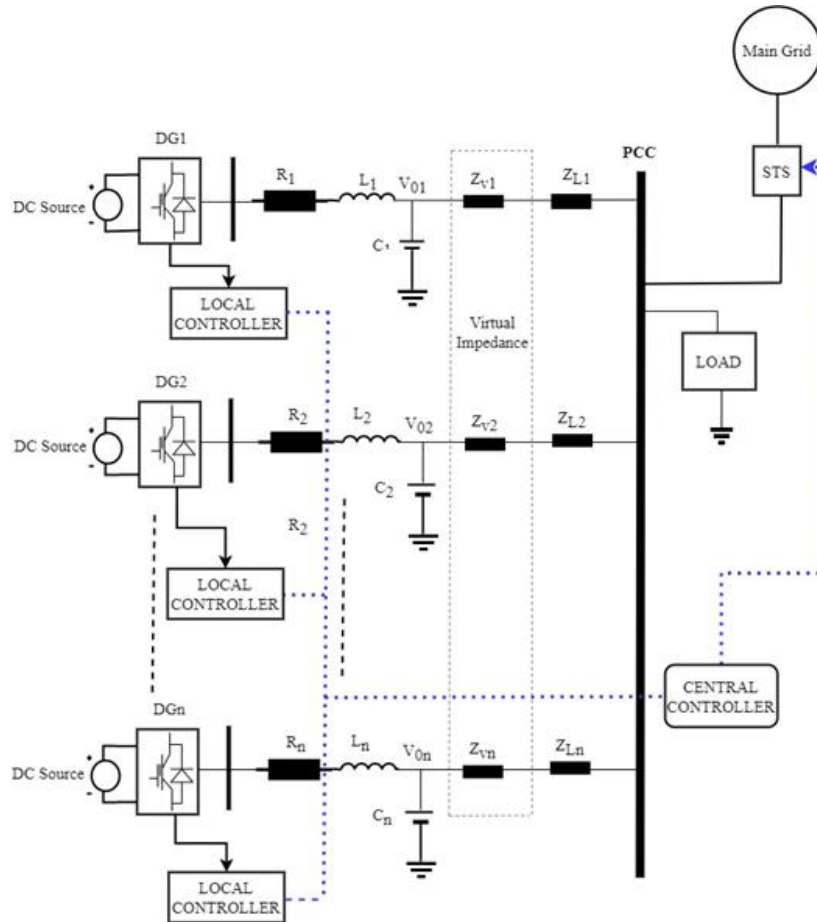


Figure 1. Microgrid with the local and central controller

2.2. Conventional droop control

Power frequency ($P-\omega$) droop curves are shown in Figure 2. In VSI model, the magnitude of frequency and voltage is regulated through $P-\omega$ droop control in islanded microgrids [24, 25]. Given the below (1) and (2) shows the relation between power and frequency.

$$\omega = \omega_o - mP \quad (1)$$

$$m = \frac{\Delta\omega}{P_{max}} \quad (2)$$

Where ω_o and ω are the nominal and angular frequency, respectively. m and P_{max} are the droop coefficient and maximum active power. Similarly, for the reactive power, the relation between voltage and reactive power is given as:

$$V = V_o - nQ \quad (3)$$

$$n = \frac{\Delta V}{Q_{max}} \quad (4)$$

Where V_o and V are the nominal and angular voltages, respectively. n and Q_{max} are the droop coefficient and maximum reactive power.

From Figure 2, it is clear that whenever there is a smaller droop slope the larger power is provided by the DG unit. Whereas the voltage and frequency of the DG units connected in parallel are the same. During steady-state conditions, the system can easily attain the active power-sharing because of the consistent frequency of the DG units connected in parallel in a Microgrid. However, the reactive power-sharing is not easily attained because of the mismatch in DG feeders.

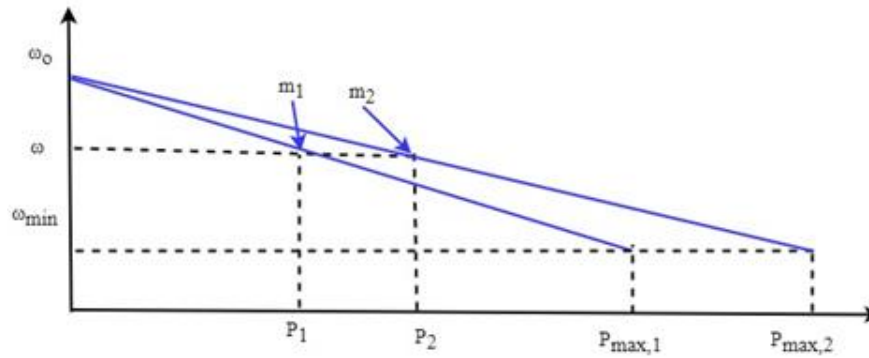


Figure 2. P- ω droop control

2.3. Reactive power sharing analysis

The voltage drop across the feeders and power-sharing deviations are addressed in [26, 27]. The mathematical expression for Voltage drop is given as:

$$V = \frac{RP+XQ}{V_o} \tag{5}$$

Where the RP and XQ are the resistance and reactance of the P and Q respectively and V_o , the output voltage of the DG unit. Considering two DG inverters DG1 and DG2 the above equation can be written as:

$$\Delta V_1 = \frac{R_1P_1+X_1Q_1}{V_o} \tag{6}$$

$$\Delta V_2 = \frac{R_2P_2+X_2Q_2}{V_o} \tag{7}$$

Mismatch in the feeder impedance is represented as follows:

$$\Delta X_1 = X_1 - X_2 \tag{8}$$

$$\Delta R_1 = R_1 - R_2 \tag{9}$$

From (5), the active and reactive powers of the DG1 and DG2 at steady state can be expressed as:

$$\Delta V_1 = \frac{(X+\Delta X)Q_1+(R+\Delta R)P_1}{V_o} = \frac{XQ_1+RP_1}{V_o} + \frac{\Delta XQ_1+\Delta RP_1}{V_o} = \Delta V_1 + \phi V_1 \tag{10}$$

$$\Delta V_2 = \frac{(X+\Delta X)Q_2+(R+\Delta R)P_2}{V_o} = \frac{XQ_2+RP_2}{V_o} + \frac{\Delta XQ_2+\Delta RP_2}{V_o} = \Delta V_2 + \phi V_2 \tag{11}$$

Where ΔV_1 and ΔV_2 are the voltage drop across the feeder 1 and feeder 2 connected with the DG1 and DG2 respectively. $(X + \Delta X) + (R + \Delta R)$ are the voltage drops across each feeder, and the term ϕV is the voltage drop mismatch.

3. VIRTUAL IMPEDANCE BASED CONTROL

Addition of impedance to mitigate the mismatch in feeder impedance is compensated by the addition of a suitable value of virtual impedance is shown in Figure 3. The concept of virtual impedance-based control mechanism where the virtual impedance is employed to mitigate the impedance mismatch of the feeder connected to the parallel-connected inverter-based microgrid system. The virtual impedance improves the reactive power sharing deviations, which at that instant occur due to the impedance mismatch of feeders connected to the network. As illustrated in the given Figure 3, the instantaneous voltage V_{Droop} can be obtained from the (1) and (3), using voltage and frequency terms,

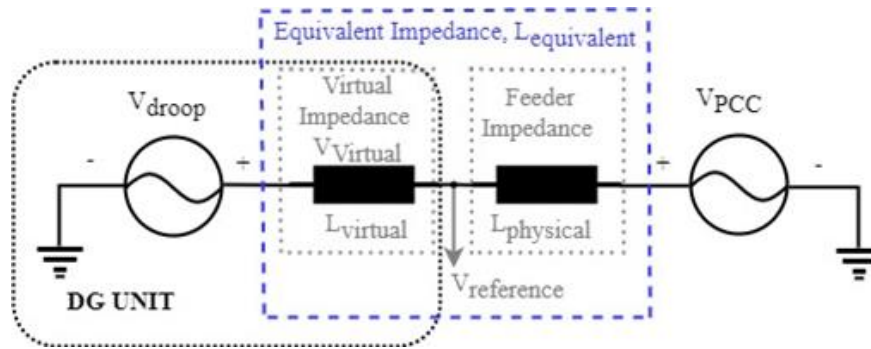


Figure 3. The equivalent impedance of DG unit

$$V_{Droop} = V \sin(\int \omega dt) \quad (12)$$

The total impedance of the feeder is the combination of Physical and virtual impedance so,

$$L_{Total} = L_{physical} + L_{virtual} \quad (13)$$

Corresponding voltage drop considering the line current is given as:

$$V_{virtual} = -\omega L_{virtual} I_{line} \quad (14)$$

The voltage and current relation are expressed as;

$$V_L(t) = \frac{L(di_L(t))}{dt}, \text{ when } i_L(t) = I_{maximum} \sin(\omega t) \quad (15)$$

$$V_L = \omega L I_{maximum} \sin(\omega t + 90^\circ) \quad (16)$$

Finally, the voltage reference with $V_{virtual}$ and V_{Droop} can be expressed as:

$$V_{Ref} = V_{Droop} - V_{virtual} \quad (17)$$

Where the reference voltage is calculated as the difference of droop voltage and virtual impedance voltage.

4. PROPOSED ADAPTIVE VIRTUAL IMPEDANCE CONTROL

Virtual impedance is dependent on pre-knowledge of feeder impedance at every time interval with fix values. Calculation of the accurate knowledge of the feeder impedance in real-time may always be not accurately calculated, and the error chances are high. Moreover, the MGs do not afford any static configuration because the connected load are not always same, it keeps on changing, and the MGs should have online impedance estimation, but unfortunately, the online estimation technique makes the controller very complex. So, to avoid the complexity of controller and computational burden, an algorithm that should not depend on pre-knowledge of feeder impedance connected to the system is proposed here. Figure 4 shows the adaptive virtual impedance control technique. The MGCC gets all the information through a low bandwidth communication link related to reactive power outputs, i.e. Q_1, Q_2 up to Q_n in this control technique. The total Q is obtained at MGCC, and the value of Q for each inverter can be determined as;

$$Q^* = \frac{Q_{Total}}{\sum_{j=1}^n Q_{Rated,j}} Q_{Rated} \quad (18)$$

Where Q^* is the reactive power demand and (18) shows the reactive power demand calculation for inverters connected in parallel in MGs individually. The difference of the reactive power demand (Q^*) and reactive power (Q), which is utilized to compensate for the DG virtual impedance. The compensation is done through an Integral controller. $L_{virtual}$ for DG inverter can be obtained as;

$$L_{Virtual} = L_{Virtual}^* + \frac{K_{iQ}}{s} (Q - Q^*) \tag{19}$$

Where $L_{Virtual}$, is the fixed virtual impedance and K_{iQ} is the integral gain (IG). The IG is set to set virtual reactance.

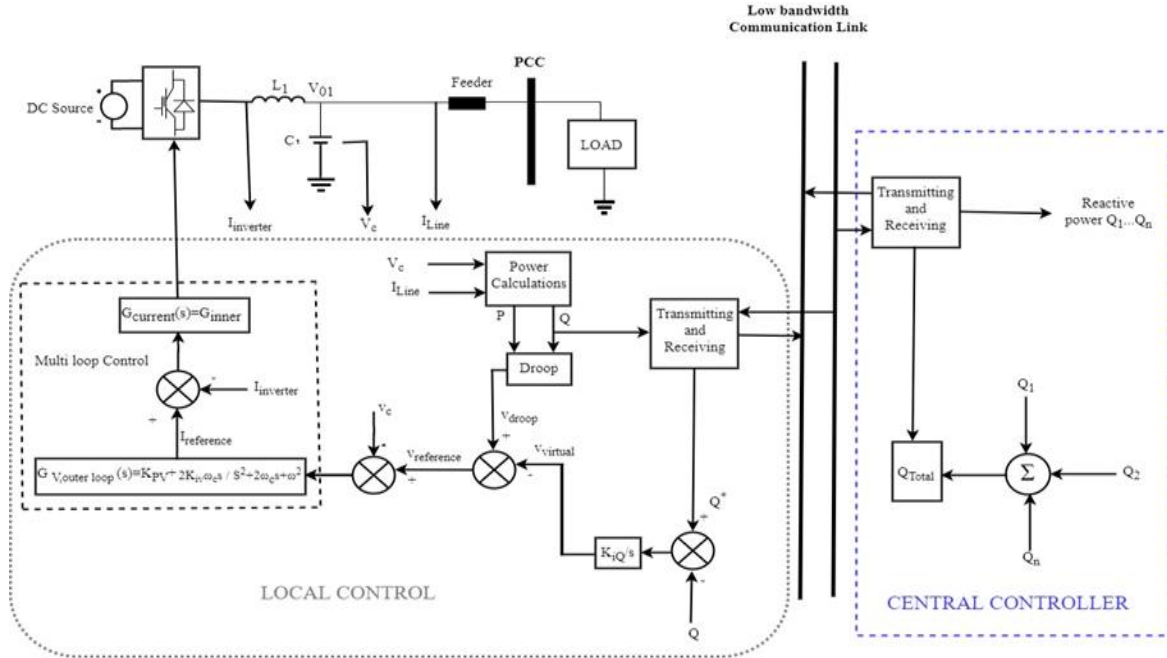


Figure 4. The proposed multiloop control scheme

In this paper, a voltage tracking technique to mitigate the reactive power sharing errors is proposed, which is implemented through the multiloop voltage controller. The reference for voltage is changed, as given in (20), the proportional resonant (PR) controller is adjusted accordingly.

$$G_{V,outer loop} = K_{PV} + \frac{2K_{iv}\omega_c s}{s^2 + 2\omega_c s + \omega_{DG}^2} \tag{20}$$

Where K_{PV} is outer loop proportional gain, K_{iv} , PR controller gain and ω_c is the cut-off frequency of PR controller. As the inner loop control gain is to be taken as $G_{current}(s) = K_{inner}$ for the current of the filter inductor.

5. SIMULATION RESULTS

The proposed multiloop virtual impedance control scheme is tested and verified through simulation results. The system parameters are listed in Table 1 given below. Two DG inverters DG1 and DG2 are tested under various conditions. The MGCC exchanges the required information through low bandwidth communication. Under the same power rating and different feeder impedances, the MG model is tested to prove its efficiency. From Figure 5 (a) and Figure (b) it is shown clearly from the figure that active and reactive powers are delivered by each DG inverter connected to the MGs in parallel. In an ideal case, both the DG inverters should have to share equal power but due to mismatch in feeder impedance power-sharing is not accurate as per DG inverters capacity. It can be seen that there are steady-state deviations inactive power-sharing. In impedance mismatch conditions, p-f droop does not show power-sharing accuracy due to the feeder's different resistances. In Figure 5 (a) steady-state deviations in P sharing. Unlike P, the Q sharing accuracy is dependent to the system frequency which is globally taken as a fixed quantity that is the reason Q sharing accuracy is better than P as given the Figure 5 (b). Initially, reactive power-sharing shows accuracy with comparison to the active power-sharing because Q depends on the frequency. When load changes applied at t=1.53 second, that changed the waveform of reactive power as well as the active power sharing accuracy deviates more. Results in Figure 6 (a) and (b) shows the performance of the proposed control

scheme to mitigate the power-sharing deviations. It is clear from the figure that P&Q deviations were removed to large extent after applying the virtual impedance multiloop technique.

Table 1. System parameters

Description	Value
DC-Link voltage, V	150 Volts
Switching frequency, f_s	10 kHz
Output Filter Parameters, R, L and C	0.1 Ω , 2 mH, 40 μ F
Nominal Voltage Amplitude, V_o	311 Volts
LPF Cut-off frequency, ω_c and Nominal Frequency, f	10 Hz and 50 Hz
Frequency, Voltage droop coefficients, m,n (Rad/Sec.w, V/var)	0.002,0.0025
Integral Gain, K_{iQ} and Fix virtual impedance $L^*_{virtual}$	1mH and 0.00006
Outer loop Proportional gain, K_{pv} and Inner loop Proportional gain, K_{inner}	0.1 and 25
Resonant controller gain, K_{iv}	18
DG Feeder 1 parameters and DG Feeder 2 parameters	3.0 mH, 0.2 Ω & 2.0 mH, 0.1 Ω

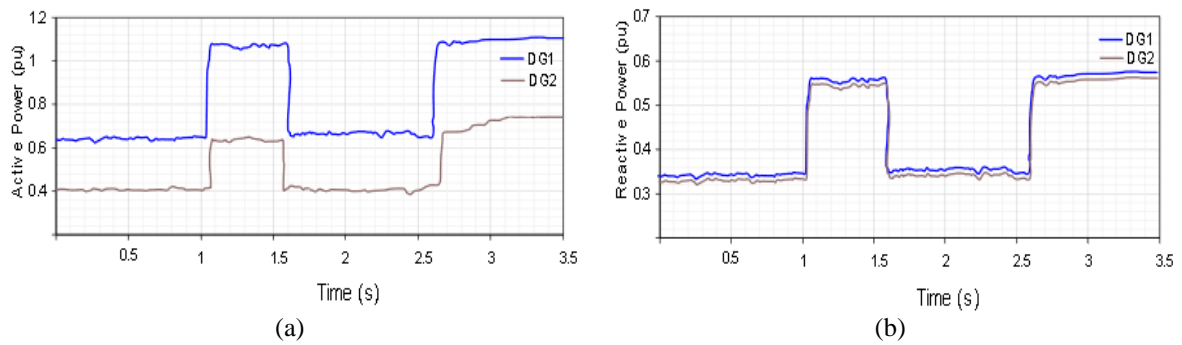


Figure 5. Conventional droop control

At $t=0.54$ seconds, the proposed virtual impedance multi loops control is applied to MGs. P & Q sharing accuracy is achieved after a short transient and even after load step changes the accuracy in power-sharing does not validate as shown in the figure the controller is enabled at $t=0.5$ second and disabled at $t=2$ seconds.

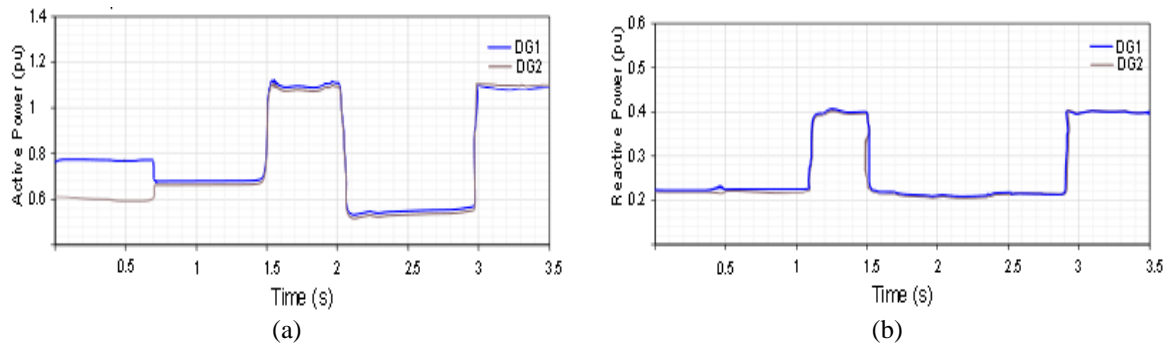


Figure 6. Multiloop control with different load variations

In Figure 7 (a), Figure 7 (b) and Figure 7 (c) it can be seen that virtual impedance loop is capable of keeping the last value even after disabling the proposed controller and the voltage at PCC is in acceptable shape and range. So, the effect of feeder impedance mismatch is removed by implementing the proposed control scheme.

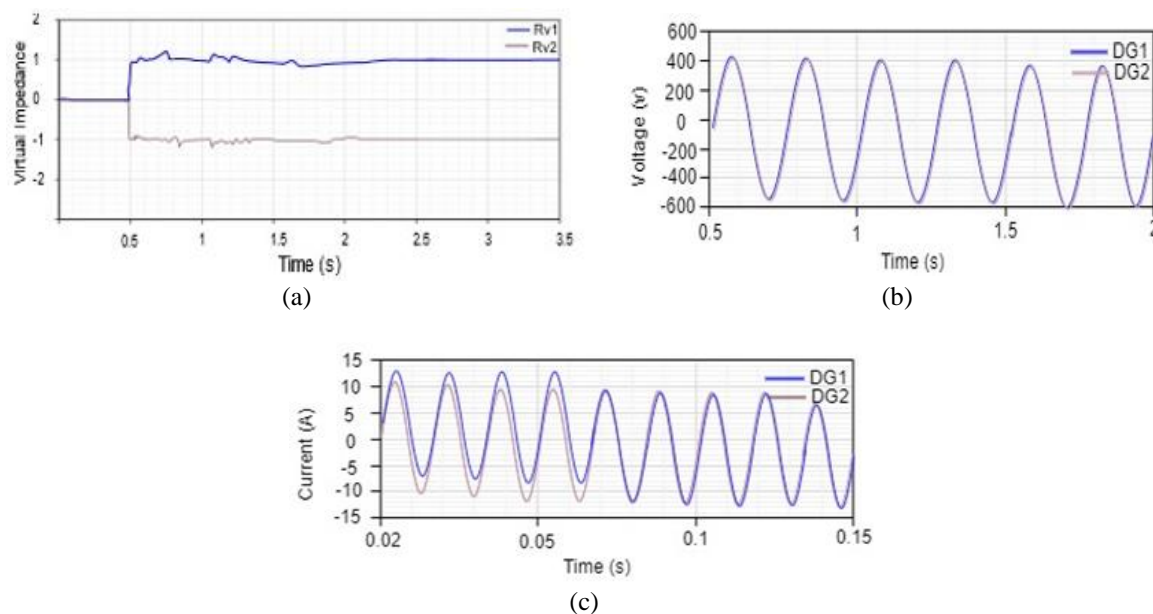


Figure 7. Virtual impedance and voltage at PCC

6. CONCLUSION

The power sharing accuracy in an islanded microgrid is a big challenge, especially the reactive power sharing accuracy. This paper proposed a low bandwidth communication based multiloop virtual impedance-based control strategy, which is implemented to two DG inverters with the same capacity and different impedances. The proposed control scheme is capable of changing adaptively when there is load change. Once the controller mitigates the mismatch of feeder impedance, the impedance values of the controller remain the same even after the controller is disabled. The proposed technique does not need any pre-knowledge of the feeder impedance, avoid complexity and computational burden. Moreover, the system stability is not affected even a low communication bandwidth is used in the system to establish a communication link between DG inverters and Microgrid central controller.

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