Integrated hybrid optical networking for 5G access networks

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ABSTRACT

Today, deployment of optical fiber has offered large transmission capacity which cannot be efficiently utilized by the electronic switches. Rather, integrated hybrid optical network (IHON) is a promising approach which combines both packet and circuit switching techniques. As a result, it achieves efficient utilization of the bulk capacity and guarantees absolute quality of service (QoS) by optimizing the advantages of the two switching schemes while diminishing their disadvantages. Transpacket has developed a fusion node implementing IHON principles in Ethernet for the data plane. Hence, this paper investigates and evaluates IHON network for 5G access networks. The simulated results and numerical analysis confirm that the packet delay variation (PDV), delay and packet loss ratio (PLR) of guaranteed service transport (GST) traffic in IHON network met the requirements of 5G mobile fronthaul using CPRI. The number of nodes in the network limits the maximum separation distance between base band unit (BBU) and remote radio head (RRH), link length; for increasing the number of nodes, and the link length decreases. In addition to this, we verified how the left-over capacity of fusion node can be used to carry the low priority packets and how the GST traffic can have deterministic characteristics on a single wavelength by delaying it with fixed delay line (FDL). For example, for $L_{\text{SM}}^{1GE}=0.3$ the added statistical multiplexing (SM) traffic increases the 10GE wavelength utilization up to 89% without any losses and with SM PLR=$1E^{-03}$ up to 92% utilization.

Keywords: 5G
Access networks
IHON
QoS

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1. INTRODUCTION

In recent years, the dynamic growth of fiber deployment has brought more transmission capacity in to hand, which is beyond the processing capacity of electronic switches [1]. As a result, fiber switching technologies [2], have been introduced to overcome the electronics processing and switching bottlenecks. Even though optics offers bulk capacity, the utilization is inefficient due to slow adaptation of wavelength switching for burst traffic. All-optical switching is believed to enhance the efficiency, but it is not practically deployed [1]. Hence, a new adaptive design approach, the hybrid optical architecture [1], was proposed: it combines the best features of both optical circuit and packet switching while diminishing their demerits to provide better performance and cost reduction [1]. Integrated hybrid optical network (IHON) is one type of hybrid network which completely integrates both circuit and packet technologies, and uses the same wavelength to transmit both traffic. As a result, IHON offers guaranteed quality of service (QoS) and efficient utilization of the transmission capacity through effective management.

Managing different technologies separately increases both operational expenses (OPEXs) and capital expenses (CAPEXs) [3]. To overcome such challenges, software-defined network (SDN) [4], was adopted: SDN separates the data and control planes, thus the network functions and protocols are made programmable. The main principle in SDN, is abstraction of the data plane using controller, and present it to applications as a

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virtual entity. As a result, the underlying infrastructure merely forwards traffic based on rules from the central controller. Moreover, the SDN scenario controls network resources and data transmission across heterogeneous domains, thus it avoids interoperability problems between network elements (NEs) from different vendors [4].

To efficiently utilize the growing transmission capacity, Transpacket has developed Fusion solution (i.e., H1 nodes) [5], based on the principles of IHON. H1 offers efficient capacity utilization while providing absolute QoS for critical traffic. So far, NETCONF-based network management system (NMS) [6], is implemented for fusion networks to supervise and manage different traffic flows.

With packet-based network realization, the performance of H1 node/fusion node issue has been one of the biggest challenges. As a result, several continuous researches have been made towards an IHON network for future networks. Using ethernet in the fronthaul [7] has been proposed to take some advantages: lower cost equipment, shared use of lower-cost infrastructure with fixed access networks, obtaining statistical multiplexing, and optimized performance. Despite of their attractive advantages, ethernet also comes with their own challenges: achieving low latency and jitter to meet delay requirements, and ultra-high bit rate requirements for transporting radio streams for multiple antennas in increased bandwidth [7]. For the above reasons, the current fronthaul/access networks are increasingly integrating more cost-effective packet switched technology, especially ethernet/internet technologies. In addition to standard ethernet switch used by ethernet, there is another node, H1, developed by transpacket [8] employed in IHON fusion solution. Fusion solution/IHON that uses standard ethernet technology rather than all-optical switching technology provides the fusion properties of circuit and packet switching network in packet network. It enables Ethernet transport and ensures strict QoS for guaranteed service transport (GST) traffic, and optimize resource utilization by introducing statistical multiplexing (SM) traffic in the unused capacity.

A number of researches have done in optical packet switched networks [9-15]. The papers focused on optimizing PLR by using different QoS differentiation policies and scheme of transport networks. The main focus of this paper is to analyzing the different QoS performance metrics that are vital in evaluating the IHON node which will be employed in ethernet-based access transport network. To the best of author’s knowledge, the IHON node performance is evaluated using simulation for the first time.

In this study, we are inspired to evaluate IHON/fusion networks by replacing the existing access network with C-RAN using simulation for 5G access networks. The authors have also highlighted the mobile fronthaul network options for 5G network discussed in [16]. In particular we focus on three performance metrics to evaluate the IHON: PLR, latency and PDV.

2. RESEARCH METHOD

The study has collected secondary data from previously published studies. These materials are background research, conference papers, white papers, International Telecommunication Union-Telecommunication (ITU-T) and Internet Engineering Task Force (IETF) standard recommendations. This study has focused on collecting the QoS requirements of IHON LAN.

Generally, Figure 1, the flow used to achieve the objective of the work includes:

![Methodology flow chart](image)

Figure 1. Methodology flow chart
1) System modeling: includes the designing, implementing, evaluating and analyzing of a single IHON node for 5G access fronthaul network.
2) Performance analysis: includes performance analysis of IHON node with respect to delay, packet loss ratio and packet delay variation.
3) Comparison: comparing of the results obtained from the simulator with IEEE 802.1CM standard and IEEE 1904.3 Task Force.
4) Interpretation of the results: the performance analysis of IHON network for 5G access networks, comparison and the interpretation of the results will be explained.

3. SYSTEM MODEL FOR SIMULATION

To study the performance of IHON node while transporting GST and SM traffic, the traffic has been transported and analyzed on a 10 GE output wavelength. Figure 2 illustrates how these traffic has been generated, and utilize the output link. Firstly, the GST and SM traffic were generated. After generating, the GST traffic was sent to the 10 GE port of the IHON node while the SM traffic was queued in a buffer until a suitable gap was detected. Secondly, both the GST and SM traffic was processed and sent out through the same output port.

Figure 2. Diagram illustrating how the IHON node is connected to Packet generators for measuring the performance metrics

3.1. Simulation model for IHON node

In this work, we simulate IHON node based on non-preemptive scheduling using the programming language called Simula/Demos programming. The software is designed and implemented as a full scale general purpose programming. As its name implies, it is an object-oriented simulation language developed at the Norwegian Computing Center in Norway for designing of simulation entities. It has also been used in simulating Very Large-Scale Integration (VLSI) designs, process modeling, and other applications [17].

3.1.1. Traffic patterns

In the IHON node implementation, the arrival processes of GST and SM traffic are described by negative exponential distributions, where as their packet lengths are described by deterministic and uniform distributions respectively. The GST packets are allowed to travel into a network following reserved wavelength available so that they do not experience any packet loss. The rate of GST traffic source is defined using negative exponential distribution with a mean value which is a function of capacity of the output port and the length of GST packet. Hence, it ensures the wanted load from GST traffic on single wavelength. Unlike GST packets, since SM traffic has no pre-established path it may experience a packet loss and is inserted in suitable gaps between GST packets. And its mean value is a function of the channel capacity and length of SM packets drawn from the distribution.

Similarly, negative exponential distributions are used to describe the arrival processes for HP and LP traffic in Ethernet switch implementation. The packet length distribution used to describe the length of HP and
LP packet is deterministic distribution. HP traffic is given highest priority, and LP traffic is given low priority. The mean value for HP and LP traffic in Ethernet switch follow the same pattern as GST and SM traffic in IHON node.

4. SIMULATION PARAMETERS

Table 1 and Table 2 present the set of parameters which have been used during the simulation and the list of notations used for different traffic loads on different interfaces respectively.

<table>
<thead>
<tr>
<th>Parameters</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Seed values</td>
<td>907 234 326 104</td>
</tr>
<tr>
<td></td>
<td>711 523 883 113</td>
</tr>
<tr>
<td></td>
<td>417 656</td>
</tr>
<tr>
<td>Output link capacity</td>
<td>10 Gb/sec</td>
</tr>
<tr>
<td>Minimum SM length</td>
<td>40 Bytes</td>
</tr>
<tr>
<td>Length of GST packet</td>
<td>1200 Bytes</td>
</tr>
<tr>
<td>Maximum SM length</td>
<td>1500 Bytes</td>
</tr>
<tr>
<td>Load of GST traffic</td>
<td>Varies</td>
</tr>
<tr>
<td>Load of SM traffic</td>
<td>Varies</td>
</tr>
<tr>
<td>Buffer size</td>
<td>16 MByte</td>
</tr>
<tr>
<td>Number of packets</td>
<td>40,000</td>
</tr>
</tbody>
</table>

Table 2. Notation of parameters used in the simulation result analysis

<table>
<thead>
<tr>
<th>Description</th>
<th>Notations</th>
</tr>
</thead>
<tbody>
<tr>
<td>The load of SM traffic on 1 Gb/s interface</td>
<td>( L_{1GE}^{SM} )</td>
</tr>
<tr>
<td>The load of GST traffic on 10Gb/s interface</td>
<td>( L_{10GE}^{GST} )</td>
</tr>
<tr>
<td>The load of GST and SM traffic on 10 Gb/s interface</td>
<td>( L_{10GE}^{T} )</td>
</tr>
</tbody>
</table>

5. RESULTS AND ANALYSIS

In this work, we considered PDV, PLR, and average latency as a performance metrics to evaluate performance of IHON node.

5.1. IHON Performance

5.1.1 GST Traffic Performance

In this part, the performance of GST traffic is presented with respect to: average latency, PLR, and PDV.

a) Average latency

In IHON node, latency refers to the delay when the first bit of the packet gets into the IHON node until the first bit of the packet leaves the FDL. This parameter greatly affects how usable the nodes as well as communication are. Every sub-simulation returns an average value for latency of GST packet, obtained by averaging all the delays experienced by every single GST packet during the sub simulation. By varying the system load \( n \cdot L_{1GD}^{SM} + L_{10GE}^{GST} \), we observed the average latency of GST traffic. The simulated average latency of GST traffic was constant with a value of 1.2 \( \mu \text{sec} \), which equals to the FDL time. It shows that the 10GE GST traffic was shown to be independent of the added SM traffic and its load. This is due to the fact that the GST traffic is transmitted with absolute priority. Note that the FDL time was set to the service time of maximum packet length of SM packet, \( \delta =1.2 \mu \text{sec} \). The average latency of GST traffic isn’t affected by the system load and insertion of SM traffic. Thus, the result reveals that its average latency is constant regardless of the node congestion.

b) Packet delay variation

As per ITU Y.1540, "PDV is the variation in packet delay with respect to some reference metrics (minimum delay in this work)". The service quality and PDV tolerability of an application are highly influenced by PDV. By recording the time when the first bit of GST packets arrived at the delay line, and when the last bit of the packet left the start of the FDL, the maximum and minimum delay of GST packet is computed. Using this concept, the result of the simulator showed that the packet delay variation of GST packets was zero. This is because the traffic aggregation of SM traffic on the top a 10 Gb/s wavelength is done without introducing PDV to the GST traffic of following the wavelength. Furthermore, the inter-packet gap between the GST
streams is preserved, and the GST streams are sent out precisely as it arrived. Consequently, it results in zero packet delay variation. Like the average latency, the PDV of the GST traffic isn’t affected by the system load and insertion of SM traffic. And also, GST packets in IHON node undergo a fixed delay of 1.2 μsec, corresponding to the service time of a maximum length SM packet. At the output wavelength, all GST packets experience this fixed delay. Hence, it doesn’t introduce PDV to GST traffic.

c) Packet loss ratio

When one or more transmitted packets fail to arrive at their destination, packet loss occurs. In IHON node, it is typically caused by blocking and congestion. Since different applications have different PLR tolerability, it has noticeable effects in all types of communications. Packet loss is measured as a percentage of the number of lost packets with respect to the total transmitted packets.

The simulation result showed that the PLR of GST traffic was zero, i.e. all GST traffic generated by the source were received at the output port of the IHON node. This is because GST packets pass through FDL, which gives time to the monitoring module to calculate the gap length between GST packets. SM packets are scheduled only if the gap is sufficient to transmit the packet. Hence, IHON nodes avoid losing of GST packets.

5.1.2. SM Traffic Performance

By varying the load of GST traffic, we observed the performance of SM traffic for a fixed SM load.

a) Average latency

For low SM load, Figure 3a, the average latency increases from 1.2 μsec to 18 μsec when the GST load, \( L_{GST}^{10GE} \), was increased from 0.1 to 0.89 with an increasing interval of 0.1. However, when we increase \( L_{SM}^{GE} \) from 0.1 to 0.3, Figure 3b, the average latency was further increased. Increasing more SM traffic, after the system load 0.8, will cause a buffer overflow; this leads the average latency to increase exponentially and causes a packet loss.

As can be seen from Figure 3, the average latency of SM traffic is increased for increasing the GST load. The increment of GST load adds more traffic to the output wavelength which in turn increases the waiting time of SM packet at the node. This means that increasing GST load will decrease the chance of SM traffic to be inserted. Generally, the higher the GST traffic load, the longer the average latency of SM traffic.

When the system load is low, then the buffers inside the node will never be full. In this simulation, since we are using variable SM packet length (40 to 1500 bytes), the average latency depends on the packet length. It means that the latency of sending a 40-byte packet and a 1500 byte is different depending on the packet length.

b) Packet delay variation

As it has been mentioned, PDV influences service quality of an application. Thus, the average PDV of SM packets were acquired from the simulation for analysis. The resulting Figure 4 shows that when the GST load of the system load traffic increased, the PDV has increased from 20.1 μsec to 411.232 μsec. This indicates that the PDV of SM packet is influenced because of the service classification, i.e. traffic load and SM traffic scheduling algorithm [18-19].

c) Packet loss ratio

One of the most important factors in the analysis of IHON node performance for SM traffic is illustrated in Figure 5. Based on the figure, no PLR was observed within system load interval [0, 0.89]. When the system load reaches 0.89, SM packets start getting dropped. From that point onward buffer overflow causes the loss to increase exponentially.
Figure 3. Average latency of SM traffic as function of GST load for SM load=0.1, 0.35 and 0.4

Figure 4. Packet delay variation of SM traffic as function of GST load
Figure 5. PLR of SM traffic as function of GST load

For $L_{SM,1GE}=0.3$, the added SM traffic increases the 10GE wavelength utilization up to 89% without any losses and with SM PLR=$1E^{-03}$ up to 92% utilization.

5.2. Evaluation of IHON network for access networks

a) Average latency

The first performance metric used for evaluation of IHON network for access network C-RAN is average latency. According to IEEE 802.1CM and Table 3, the maximum E2E latency between BBU and RRH required for mobile fronthaul is specified as 50 $\mu$sec (including fiber length and PDV), and the latency budget is 5$\mu$sec when cable propagation is excluded. Within this 50 $\mu$sec latency, the maximum separation distance between BBU and RRH (link length) is limited and depends on the number of nodes in the network.

<table>
<thead>
<tr>
<th>N</th>
<th>$D_{node}$ ($\mu$sec)</th>
<th>$L_{total}$(Km)</th>
</tr>
</thead>
<tbody>
<tr>
<td>2</td>
<td>2.4</td>
<td>9.52</td>
</tr>
<tr>
<td>3</td>
<td>3.6</td>
<td>9.28</td>
</tr>
<tr>
<td>4</td>
<td>4.8</td>
<td>9.04</td>
</tr>
<tr>
<td>5</td>
<td>6.0</td>
<td>8.8</td>
</tr>
<tr>
<td>6</td>
<td>7.2</td>
<td>8.56</td>
</tr>
</tbody>
</table>

Table 3. Maximum link length and number of nodes in IHON network to meet the fronthaul requirements for GST traffic where $L_{total}=50\mu$sec, $D_{r}=5\mu$sec/km, and $D_{node}=1.2\mu$sec

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To calculate the link length as function of number of nodes (N), the following equation has been used:

\[ L_{\text{total}} = N \times (D_{\text{node}} + \text{Delay}_{\text{PDVnode}}) + D_T \times \text{Link}_{\text{length}} \]  

(1)

where, N = Number of nodes.

- \( D_{\text{node}} \): Latency in a single node, 1.2\( \mu \)sec (obtained from the simulation).
- \( \text{Delay}_{\text{PDVnode}} \): PDV in a single node, 0\( \mu \)sec (obtained from the simulation).
- \( D_T \): Transmission latency, 5\( \mu \)sec/km.
- \( L_{\text{total}} \): Maximum E2E latency, 50\( \mu \)sec.
- \( \text{Link}_{\text{length}} \): Maximum link length.

Table 3 presents the relationship between the number of nodes in IHON fronthaul network and the maximum separation distance between BBU and RRH for GST traffic. In the table, the second column indicates the overall delay in the nodes, whereas the third column is the link length between BBU and RRH in the network. The table shows that for increasing the number of nodes in the network, the link length decreases. For instance, for IHON network with 3 nodes require 9.28 Km fiber link length. Similarly, for IHON network with 4 nodes require 9.04 Km fiber link length.

The simulation results and Table 3 proved that IHON networks are capable of carrying radio signals over packet-based fronthaul network provided that the number of nodes in the table corresponds to its respective link length. Unfortunately, obtaining the average latency of BE or SM traffic that meets fronthaul requirement is not so straightforward, since this traffic depends on a load of GST traffic.

b) Packet delay variation

Another performance used for evaluation of IHON network for a fronthaul network is PDV. The maximum PDV specified is 5\( \mu \)sec or 10% of E2E latency. The simulated result of PDV for GST traffic was zero. Thus, the GST traffic class meets the fronthaul requirement in PDV comparison. The fronthaul requirement is higher than peak PDV of GST traffic for any system load in the interval \([0, 0.99]\). Consequently, of the three performance metrics (average latency, PDV, PLR), the IHON network performs best in PDV for GST traffic. It has no restriction in wavelength utilization of IHON network.

c) Packet loss ratio

At last, the performance evaluated is PLR. Fronthaul networks have a very strict PLR requirement which is in the interval \([10^{-6}, 10^{-9}]\). As described, GST packets experience no packet loss regardless of the network congestion. So, the GST traffic is transported through the IHON fronthaul network with absolute priority. In our simulation, GST packet loss wasn’t observed at any system load, while SM packets getting dropped for system load beyond \( L_{10GE}^{-1} = 0.89 \) for SM load=0.3. With system load in the interval \([0, 0.99]\), GST traffic class meets the fronthaul requirement, and can be served properly in IHON network.

6. CONCLUSION

In this work, the use of IHON networking has been evaluated and investigated to employ it in 5G access networks. The simulation and numerical analysis result verified the effectiveness of IHON network. In the analysis of IHON node, the performance of delivering high quality of service was approved and reflected on GST and fits the technical specification of fusion/IHON node. On the other hand, SM traffic is suitable for transporting time insensitive information. The recommendation from the IEEE 802.1CM standard and other articles were considered for examining and investigating how IHON access network networks should provide the required quality of service. Other important points that this work has measured and evaluated: the important design concepts of fusion node have been confirmed, the fusion node aggregation of multiple 1GE SM traffic and 10GE on a single wavelength has been achieved without affecting the deterministic nature of circuit switching, the simulation result confirms that the average latency of GST packet is independent of the system load and experiences a zero packet delay variation in a node, and the efficient utilization of bandwidth has been achieved by inserting suitable SM traffic in the computed gap between GST packets.

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CONFLICT OF INTEREST

The authors declare that there are no conflicts of interest.

REFERENCES


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