Microstrip array antenna with inset-fed for WLAN application

Norfishah Ab Wahab, W. Nor Syafizan W. Muhamad, Zuhani Ismail Khan, Suzi Seroja Sarnin Faculty of Electrical Engineering, Universiti Teknologi Mara (UiTM), Malaysia

Article Info	ABSTRACT
<i>Article history:</i> Received May 1, 2019 Revised Jul 20, 2019 Accepted Aug 7, 2019	This paper proposed three designs of microstrip array patch antennas, to resonate at 2.4 GHz. The purpose of the study is to achieve size reduction with acceptable performance for wireless communication system applications. Based on the array concept, the array antennas are arranged using corporate network technique. It is found that the simulated 4x3 patch array antenna achieved the compact size with dimension reduced up to 26%
<i>Keywords:</i> Array antenna Inset-Fed Microstrip Patch antenna	compared to $4x1$ and $4x2$ array patch antennas. In terms of return loss, the antenna attenuated more than 19 dB. The $4x3$ patch array antenna is fabricated and measured using RO4350 microstrip substrate to validate the concept. The responses are found in good agreement between simulation and measurement.
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Corresponding Author:

Norfishah Ab Wahab, Faculty of Electrical Engineering, 40450 Shah Alam, Selangor, Malaysia. Email: fishahahu@gmail.com

1. INTRODUCTION

An efficient and high performance antenna is very important in Wireless Communication Systems. This is due to the fact that the antenna performance is directly impact on the quality of wireless communications [1-6]. For the application of emerging technology, it requires compact in size components with low in power usage and robustness. Researched on various antenna topologies were reported. These topologies were experimented on different type of technology available today [7-12]. However, a low cost and efficient antenna with ease in manufacturing process is the main interest among the industry players. Microstrip technology is well known for its low cost, easy to materialize with acceptable antenna performance [13-16]. Based on these facts, microstrip substrate is chosen because of its low cost, compact in size with an acceptable performance level that is required by the wireless communication system [17-20]. However, there are limitations in microstrip technology such as narrow in bandwidth, low gain and normally contain spurious effect especially when dealing at frequency above 5 GHz [1, 21-23].

Numerious improvement in terms of the antenna design using microstrip technology have been proposed. Amongst these, arrays concept is one of the popular technique. The base cell is used to construct the antenna design, arranged in an array manner; using different feeding techniques available today. The findings were reported that microstrip arrays antenna is possible to achieve high gain, acceptable desired radiation pattern with appropriate beamwidth [17, 23-25].

Based on this concept, three microstrip arrays patch antenna designs are proposed in this work. With arrangement of 4x1, 4x2 and 4x3 array patch antennas are proposed. The antennas are designed at 2.4 GHz for WLAN applications. The performances of the antennas are investigated in terms of gain, return loss and compactness. To validate the concept, results are compared between simulations and measurements and are found agreeable.

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2. RESEARCH METHOD

The antennas proposed in this work are based on rectangular patch as shown in Figure 1, designed at 2.4 GHz for WLAN application. The specification of the antenna is as tabulated in Table 1. Quarter wavelength transformer is used to match the feeding line of the antennas.

Table 1. Specifications of Microstrip Patch Antenna Design of WLAN Application			
Centre frequency, f ₀	2.4 GHz		
Return Loss	>10 dB		
VSWR	1		
Substrate specifications	Subtrate Thickness=075mm, dielectric constant 3,3, loss tangent=0.004		

2.1. Single Microstrip Patch Antenna Design

Figure 1 illustrates the general view of single patch antenna with microstrip feedline. The design of the patch antenna consists of patch, quarter-wave transformer and feedline [16]. In (1-4) are applied to determine the dimensions of the single patch antenna in terms of width, W and length, L as follow [1]:

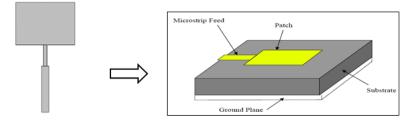


Figure 1. General view of rectangular patch antenna

Width of patch, wp is defined as follow:

$$w_p = \frac{c}{2f_r} \sqrt{\frac{2}{\varepsilon_r + 1}} \tag{1}$$

The effective dielectric constant is calculated by the formula below:

$$\mathcal{E}_{reff} = \left(\frac{\varepsilon_{r+1}}{2}\right) + \left(\frac{\varepsilon_{r-1}}{2}\right) \left[1 + 12\frac{h}{w}\right]^{-\frac{1}{2}} \tag{2}$$

The genuine length of patch, l_p is computed utilizing the accompanying formula:

$$\frac{\Delta L}{t_s} = 0.412 \frac{(\varepsilon f f + 3) \left(\frac{w_p}{t_s} + 0.264\right)}{(\varepsilon f f - 0.258) \left(\frac{w_p}{t_s} + 0.8\right)} \tag{3}$$

$$l_p = \frac{c}{2f_o\sqrt{\varepsilon_{ff}}} - 2\Delta L \tag{4}$$

As stated in theory, the distance between the edge of patch and substrate must be more than $\frac{\lambda}{4}$ in order to avoid the signal to radiate more to ground. Thus, the length, l_s and the width of substrate, w_s are computed using these formulas:

$$w_s = w_p + 2\left(\frac{\lambda}{4}\right) \tag{5}$$

$$l_s = l_p + 2\left(\frac{\lambda}{4}\right) \tag{6}$$

While the width of the quarter-wave line is obtained by [2, 10]:

$$Z_T = \frac{60}{\sqrt{\varepsilon_r}} ln \left(\frac{8d}{w_T} + \frac{w_r}{4d}\right) \tag{7}$$

Where ZT is calculated as [8, 12]:

$$Z_T = \sqrt{50 + Z_a} \tag{8}$$

The length of quarter line is calculated by:

$$L_T = \frac{\lambda}{4} = \frac{\lambda_0}{4\sqrt{\varepsilon_{eff}}} \tag{9}$$

And the width of the 50 Ω microstrip feed is found using the well-known equation below:

$$Z_o = \frac{120\pi}{\sqrt{\varepsilon_{reff} \left(1.393 + \frac{W}{h} + \frac{2}{3}ln\left(\frac{W}{h} + 1.444\right)\right)}}$$
(10)

Using microstrip feeding technique, a simple rectangular microstrip patch antenna is designed with the layout as shown in Figure 2. This patch is used as base cell to construct the arrays.

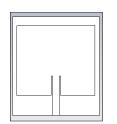


Figure 2. Single patch array design

2.2. Microstrip Patch Array Antenna Design

In designing array antenna, the base cells are arranged and feed using inset-fed technique as shown in Figure 3 while Table 2 tabulates all the calculated dimensions of the feedlines.

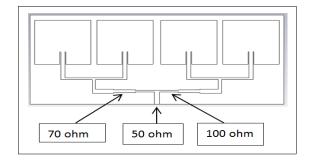
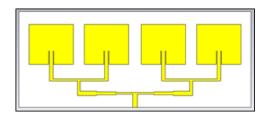


Figure 3. Patch array antenna with inset fed

Table.2. Dimension (Width and Length) of 100Ω , 70Ω and 50Ω

Impedance	Width (mm)	Length (mm)
50Ω	3.3	9
70Ω	13	2
100Ω	13	1

Figures 4, 5, and 6 illustrate the topologies of the 4x1 array, 4x2 array and 4x3 array patch antennas respectively, designed at 2.4 GHz. The antennas are simulated and the return losses, S11 of the antennas are shown in Figure 7. It can be seen that the performance of the 4x3 patch array antenna achieved the highest level of return loss which is 19.73 dB.



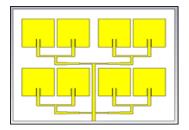


Figure 4. 4x1 patch array antenna design

Figure 5. 4x2 patch array antenna design

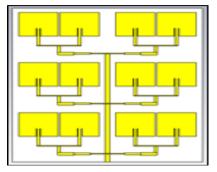


Figure 6. 4x3 patch array antenna design

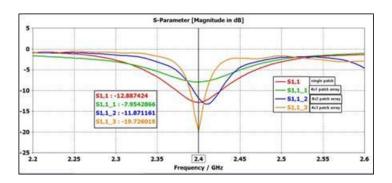


Figure 7. Comparison of return loss, S11

Table 3. Comparison of Simulation Results for All the Four Antenna Designs

	Single patch	4x1 patch array	4x2 patch array	4x3 patch array
	Single pater	antenna	antena	antenna
Size of design (cm ²)	45 x 40 = 18	157 x 65= 102.05	170 x 130= 221	275 x 145= 253.75
Return loss, S11 (dB)	12.89	7.95	11.87	19.73
VSWR	1.59	25.38	1.33	1.23
Gain (dB)	4.25	7.31	8.49	11.41
Directivity (dBi)	5.63	9.58	9.69	12.06

Table 3 summarized the simulated the return loss and gain for all the designs obtained from CST simulation tool. It has shown that the 4x3 patch array antenna achieved highest gain compared to the other three antenna designs. Therefore, the 4x3 patch array transmits the highest power in the direction to that of an isotropic source. Based on these results, the 4x3 patch array antenna is chosen and the design is fabricated, analysed and compared with simulation results to validate the work.

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3. RESULTS AND ANALYSIS

Figure 8 illustrates the simulation results of Voltage Standing Wave Ratio (VSWR) and radiation pattern for the 4x3 array patch antenna, designed at 2.4 GHz. The VSWR achieved 1.23 and the radiation pattern shows directivity and gain of 10.39 dB and 9.08 dB respectively.

Figures 9 and 10 show the radiation pattern for simulated and measured respectively for the 4x3 patch array antenna. Both the radiation patterns portray the omnidirectional antenna that suit with WLAN applications. The simulation result in Figure 8(b), shows the major lobe directed the signal at 0° with beamwidth (HPBW) of 43.3°. This antenna seems to generate more radiation power in the farfield region.

Figure 11(a) and (b) show the return loss and the prototype of the 4x3 array antenna. It is found that the measured return loss attenuated 19.85 dB at 2.427 GHz, shifted by 0.027 GHz from the simulation. The shifted of measured result as compared to simulation may be due to some flaws during fabrication process and also may due to the effect of parasitic element on the substrate. [1]. Table 4 shows the comparison between the 4x3 patch array antenna with previous designs according to return loss and gain. In terms of return loss, the proposed antenna achieved highest gain compared to the other three designs.

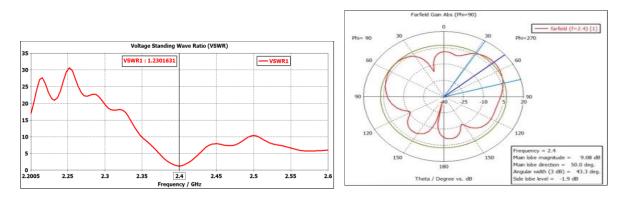


Figure 8. Simulation results of the 4x3 array patch antenna (a) VSWR, (b) radiation pattern

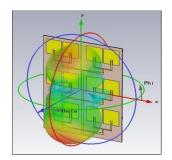


Figure 9. The 3-D radiation pattern of 4x3 array antenna

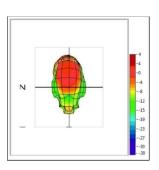


Figure 10. Measured radiation pattern (in 3-D front view) for 4x3 array patch antenna

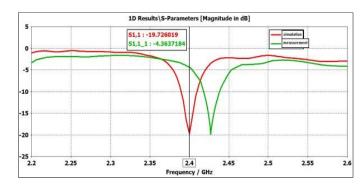




Figure 11. (a) S-Parameter: Return loss, S11 of the 4x3 array antenna, (b) Fabrication antenna

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Table 4.	Comparison	between	this	Work with	Previous	Designs

Ref	S11 (dB)	Gain
4x1 array [23]	20.24	5.73
4x2 array [24]	19.64	6.74
4x3 array: This work	19.73	9.08

4. CONCLUSION

Microstrip patch array antennas using microstrip feedline were presented. The antennas were simulated and compared in terms of gain and return loss. It was found that the 4x3 array antenna achieved best return loss and this antenna was fabricated. Results were compared between simulation and measurement and were found aggreable. Besides enriching the antenna bank, this antenna may be used for WLAN application.

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BIOGRAPHIES OF AUTHORS



Norfishah Ab Wahab was born in Malaysia, in 1963. She received the degree in Electronics Engineering from Universiti Teknologi MARA (UiTM), Malaysia, MSc in Telecommunication and Information Engineering and PhD in microwaves, electromagnetism in the same university. Currently, she is a senior lecturer in Faculty of Electrical Engineering, UiTM Shah Alam, Malaysia. She has been working in UiTM since April 2008. From 1992 to 2004, she was employed by Telekom Malaysia. She worked in various divisions such as microwave, fiber cable and network system departments. Her major research areas are in microwave and electromagnetic modeling, low-powered electronic design, energy harvesting and efficiency.



Wan Norsyafizan W. Muhamad was born in Malaysia, in 1978. She received the degree in Electrical Engineering and Master of Electrical Engineering from Universiti Malaya Malaysia and PhD in Computer Engineering from University of Newcastle, Australia in 2017. She is now a senior lecturer in Faculty of Electrical Engineering, UiTM Shah Alam, Malaysia. Her major research area is in Wireless Communication (Physical and MAC cross layer optimization).



Suzi Seroja Sarnin was born on January 28th, 1974 in Selangor, Malaysia. She received her Bachelor of Electrical and Electronics (B.Eng), in field of Communication from the Universiti Teknologi Malaysia, Skudai, Malaysia in 1999. She completed her Master of Microelectronics (Msc), from the Universiti Kebangsaan Malaysia in 2005 and obtained her PhD in Electrical Engineering from Universiti Teknologi MARA, Shah Alam, Selangor, Malaysia. She is now a senior lecturer in Faculty of Electrical Engineering, UiTM Shah Alam, Selangor, Malaysia.



Zuhani Ismail Khan received her Bachelor of Engineering in Electrical and Electronic Engineering from University of Bradford, United Kingdom and MSc in Computer and Information Engineering from International Islamic University Malaysia, Gombak in 1998 and 2008 respectively. She received her PhD in Electrical Engineering from Universiti Teknologi MARA (UiTM) Shah Alam in 2015. Currently being appointed as the Head of Department for Communication Engineering with a vison to uphold the department and faculty as an active research hub in RF and Microwave devices and systems, and Wireless Communication Technologies.