

A novel index modulation scheme with impedance matching

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ABSTRACT

For 5G and Beyond 5G, spectral efficiency is the key issue to overcome explosive data traffic. Index modulation is one of the techniques that conveys more data given to indices of status in communication transmission item such as antenna and subcarrier. In this paper, we present a novel index modulation scheme by given index to the conditions of impedance matching or not. In the transmitter, antenna chooses an impedance according to the mapping table as active impedance. In the receiver side, impedance index is detected by comparing with the result of impedance matching between received signal and receivers. In the simulation, Spectral Efficiency (SE) and Bit Error Rate (BER) were analyzed and compared to conventional modulation scheme.

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1. INTRODUCTION

Recently, future wireless network faced on the explosive increasing of data traffic [1]. According to Cisco's VNI Global Data Traffic Forecast, by 2020, wireless data traffic is expected to increase to five times the current level to 49EB (Exabyte) [2]. In order to solve this increase, many researches are focusing on how to transmit a large amount of information in a communication channel [3]. Moreover, frequency is the limited resource, so it is necessary to transmit data efficiently with limited frequency conditions. One of the way to improve spectral efficiency is modulation.

Conventionally, carrier modulation techniques have been researched by exploiting characteristic of carrier. The parameters of carrier including phase, amplitude and frequency are used one or more to modulate such as Phase-Shift Keying (PSK) and Quadrature Amplitude Modulation (QAM) which is modulated amplitude and phase at the same time [4],[5]. However, these modulation techniques can augment both error rate and design complexity of the system as the amount of information to be transmitted increment.

Index Modulation (IM) suggested to improve performance of spectral efficiency for 5G and Beyond 5G. The concept of IM is to transmit additional information by utilizing antennas, subcarriers and time slots [6], [7] etc. IM can reduce the complexity of design as well as improve spectral efficiency and energy efficiency in the system. Commonly, there are two IM scheme that a lot of research has been doing, one is Spatial Modulation (SM) [8] and the other is Orthogonal Frequency Division Multiplexing Index Modulation (OFDM-IM) [9]. SM is spatial multiplexing MIMO scheme using antenna index in order to overcome Inter Channel Interference(ICI) and improve SE [10], [11]. In transmitter, data is transmitted using one antenna in multi array antenna according to the data bits. The data can be detected both activated antenna index and data symbol in receiver. OFDM-IM is a multi-carrier transmission scheme inspiring by the concept of SM [12], [13]. The key principle of the scheme is given indices to the group of subcarrier using On-Off Keying(OOK). That means the whole carrier is divided into small groups, and information is assigned to the activated

subcarrier group by 1 and the other groups by 0 [14]. It can detect index of the activated subcarrier's group or not.

In this paper, we introduce a novel index modulation scheme Impedance Index Modulation, called IIM, by exploiting impedance matching. In general, when two circuits are connected with different impedance value, a reflection voltage is generated at the end of circuit, that is the impedance mismatch. If mismatching problem happens in the system, it is impossible to transmit maximum power, so it degrades the power efficiency [15]. To avoid this problem, impedance matching system is necessary to build between different impedances. Like this, impedance mismatch was considered to be a value to be removed from the system because of the power loss. However, in this paper, we propose a method to transmit data by using this removed value. The key point of IIM is that assigned binary1 to perfect match case and binary 0 is given mismatch case. When one impedance is selected in the transmitter, received signal is detected using comparison of impedance match or not in the receiver. The information is decoded from the mapping table using the detected impedance. Through this process, it is possible to transmit additional information by using the impedance matching index.

We describe the basic concept of impedance matching in Section 2. In Section 3, we show the proposed system divided two part, system model and impedance detecting algorithms. Section 4 contains simulation result about bit error rate and spectral efficiency. Finally, conclusions are given in Section 5.

2. RELATED WORK

At first, we introduce the basic principle of impedance matching scheme to apply our proposed system. Impedance matching is a technique, in which matches the impedance of the source and load to be equal, to transmit maximum power from source to load in the RF system[16]-[18]. This technique is essentially used for antenna and speaker design. Especially, it is an important factor in antenna design to maximize power efficiency of system by reducing power loss in RF.

Figure 1 shows a system model of impedance matching. As Shown in Figure 1(a), when source impedance Z_S and load impedance Z_L with different properties($Z_S \neq Z_L$) are connected, the reflection voltage $V_{reflected}$ is generated contact boundary. When the reflected voltage happens, the power loss of the signal occurs, moreover, the efficiency of the overall system is lowered. For this critical reason, the impedance matching network is widely used for adding between two impedances like Figure 1(b) [18].

When impedances match perfectly, there is no power loss, so the power of load impedance will be maximized. In impedance matching system, load impedance is $Z_L = R_L + jX_L$ and source impedance is $Z_S = R_S + jX_S$. The power of load impedance P_L is defined as,

$$P_L = \frac{1}{2} \frac{|V_S|^2 R_L}{(R_S+R_L)^2 + (X_S+X_L)^2} \quad (1)$$

where V_S is source voltage. Based on the (1), the load impedance is the same size as the source impedance and the opposite direction, then the load impedance power will be the maximum, that means $Z_S = Z_L$ *[19]. Thus, the maximum power transfer theory is satisfied when the impedance is the same, at that time impedance is matched.

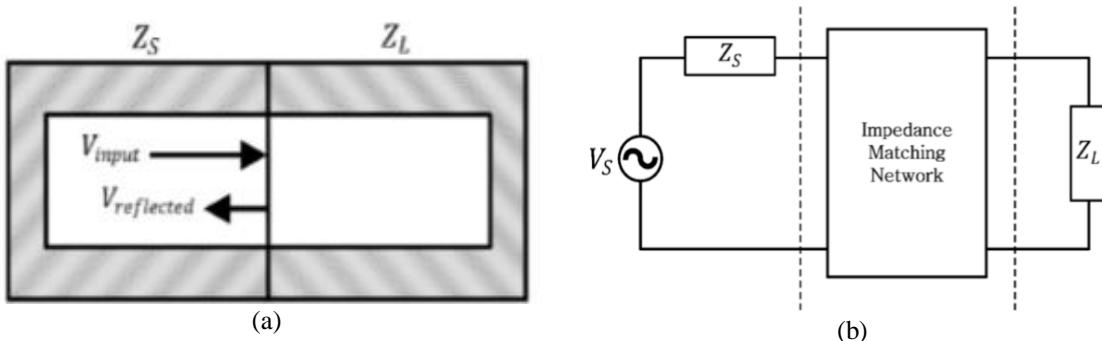


Figure 1. Effects of the impedance matching (a) reflection voltage, (b) system model

3. IMPEDANCE INDEX MODULATION

3.1. System Model

In this paper, we suggest the impedance index modulation that is inspiring by the principle of impedance matching, in order to improve spectral efficiency. Let us suppose that the channel between the transmitter and receiver is a transmission line for applying impedance matching to system model.

Figure 2 shows the proposed system model. Each transmitter and receiver has N_Z impedances ($Z_1, Z_2, \dots, Z_k, k \in \{1, \dots, N_Z\}$). Through switch, one impedance is connected to the RF antenna in the transmitter. On the other hand, all impedance is connected to the RF antenna on the received side. The block diagram of the impedance index modulation is shown in Figure 3. First, when input bits B come to system, it divided two group of bits like impedance index $B_1 = \log_2[N_Z]$ bits and conventional symbol modulation $B_2 = \log_2 M$ bits using bit splitter. The impedance index is selected according to the mapping table. The selected impedance connects to antenna using switch, which means one impedance is chose in the transmitter. We assume that the speed of impedance switching in transmitter is not affect signal transmission. When the signal transmit through the channel, the received signal go through all of receiver impedance and check the result that impedance match or not. During impedance matching process, impedance index is detected. And then, symbol can be demodulated. We describe the part of the impedance index detection in detail in the next section.

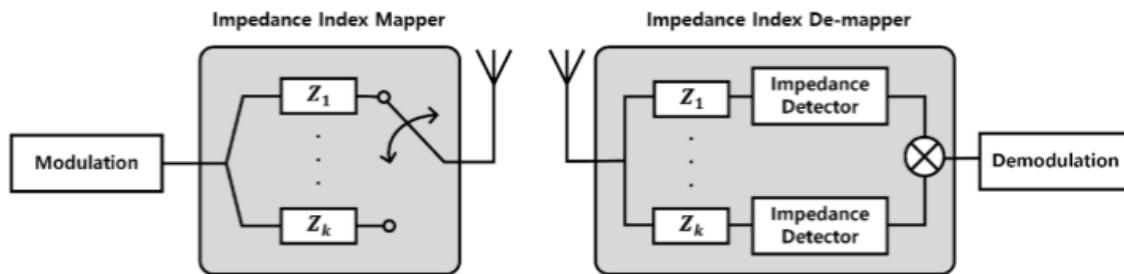


Figure 2. System model

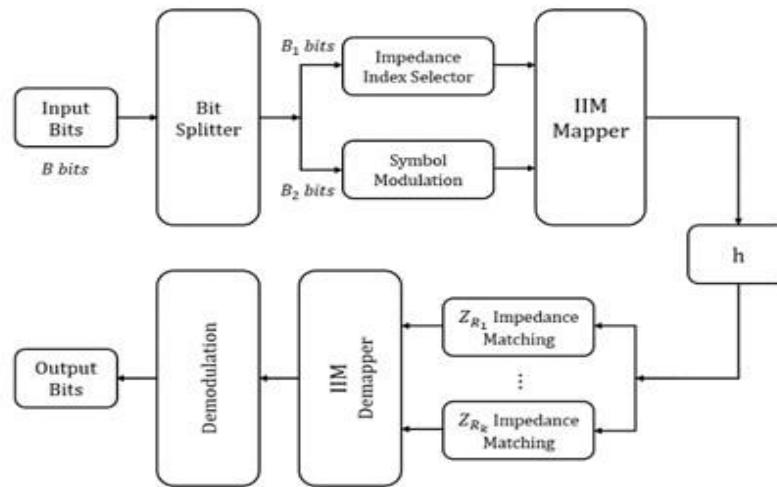


Figure 3. Block diagram of the impedance index modulation

Table 1 represent the mapping table of impedance index. The mapping table shows impedance index and symbol value according to the bits. Let us suppose the condition using four impedances and the conventional modulation scheme as BPSK. In this case, the impedance index bit is $B_1 = \log_2[4] = 2$ bits and symbol modulation bit is $B_2 = \log_2 2 = 1$ bits, so the total incoming bits is 3bits. For example, when incoming bits are '010', B_1 is first two bits as '01' that means impedance index is 2 based on the mapping table, and B_2 is last one bit as '0' that said symbol data is -1 in BPSK system.

Table 1. The Mapping Table in IIM($N_Z = 4$ with BPSK)

B_1	B_2	Bits	Impedance Index	Symbols
00	0	00	1	-1
00	1	00	1	+1
01	0	01	2	-1
01	1	01	2	+1
10	0	10	3	-1
10	1	10	3	+1
11	0	11	4	-1
11	1	11	4	+1

3.2. Impedance Index Detecting Algorithm

Figure 4 shows our proposed algorithme for detecting impedance index. The main idea of index detecting is to use maximum power transfer theorem based on the information of impedance match or not. In this section, we show how to apply the principle of the impedance match to the index modulation and detect to impedance index.

In the wireless communication system, the general received signal is defined as,

$$\mathbf{y} = \sqrt{P_T} \mathbf{h} \mathbf{x} + \mathbf{n} \quad (2)$$

Where \mathbf{h} is channel, \mathbf{n} is Additive White Gaussian Noise (AWGN) and P_T is the power of transmitter.

In case of the impedance matching, the matching result affects to load power. By applying impedance match in the proposed system, the source impedance Z_S is matched to the transmitter impedance Z_T and the load impedance Z_L will be the receiver impedance Z_R .

As shown Figures 2 and 3, when one impedance index is demodulated in the system, one selected transmit impedance is compared with all of the receiver impedances by using the received signal. Based on (1), the power of k^{th} receiver impedance P_{R_k} is described as

$$P_{R_k} = \frac{1}{2} \frac{|V_T|^2 Z_{R_k}}{(Z_T + Z_{R_k})^2}, k \in \{1, \dots, N_Z\} \quad (3)$$

Where Z_T is the transmission impedance, Z_{R_k} is k^{th} impedance of receiver, and V_T is the transmitter power. In this case, V_T and Z_T are variable value up to the received signal. Based on (3), V_T can be omitted as it does not change if the receiver impedance changes. To use easily the principle of impedance match, we derived the attenuation coefficient and gave to the weight value for each result of the impedance matching process in the receiver. Therefore, the attenuation coefficient of the transmission impedance and the receiver impedance is derived from (3) and expressed as follows,

$$\gamma_k = \frac{Z_{R_k}}{(Z_T + Z_{R_k})^2} \quad (4)$$

Algorithm 1 Detect Impedance Index

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1: procedure 1: DETECT INDEX( $Z_{index}$ )
2:   for  $k = 1$  to the number of impedance  $N_Z$  do
3:     Calculate attenuation coefficient  $k^{th}$   $\gamma$  using eq.(4)
4:     Calculated modified recived signal  $\hat{\mathbf{y}}_k$  using eq.(5)
5:     Find minimum value compared  $\mathbf{h} \mathbf{x}$  with  $\hat{\mathbf{y}}_k$ 
6:     if  $argmin||\mathbf{h} \mathbf{x} - \hat{\mathbf{y}}_k||^2$  then
7:       index  $Z_{index} \leftarrow k$ 
8:     end if
9:   end for
10: end procedure

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Figure 4. The algorithm of detecting impedance index

It can be confirmed that the impedance of receiver power becomes maximum when the transmitter and receiver impedances are matched according to the attenuation coefficient. The received power value is determined according to the attenuation coefficient, which is expressed by multiplying the received signal. When one impedance index is restored, comparison is made by the number of impedance in the receiver. In one process, the received signal by exploiting the attenuation coefficient can be expressed as (5)

$$\hat{\mathbf{y}}_k = \gamma_k \mathbf{y} \quad (5)$$

$$Z_{index} = \arg \min_k \|\mathbf{h}\mathbf{x} - \hat{\mathbf{y}}_k\|^2 \quad (6)$$

In each process, the intensity difference between the transmitted signal and the received signal is compared. If the difference has the minimum value at the k th impedance, the impedance index becomes k , and the index can be detected.

4. SIMULATION RESULTS AND DISCUSSION

In this section, the performance of the proposed scheme is evaluated by simulation. We assumed that the receiver knows the channel state information (CSI) perfectly in the Rayleigh fading channel. To evaluate the proposed scheme, M-ary phase shift keying (M-PSK) is used for the conventional modulation scheme [18], [19].

In Figure 5, we show the comparison of the spectral efficiency of the proposed system and M-PSK. The spectral efficiency is the amount of information that can be transmitted over a given bandwidth in a communication system [20]. The spectral efficiency is expressed as follows,

$$R_{IIM} = \log_2 M + \log_2 [N_Z] \quad (7)$$

Where N_Z is the total number of impedances, M is the modulation order. To verify the performance, we consider two impedances, named IIM ($N_Z = 2$), and 4 impedances, which is IIM ($N_Z = 4$) with BPSK. To match same bpcu (bit per channel use), the conventional modulation is used QPSK for 2bpcu and 8-PSK for 3bpcu. When SNR is 18dB, both of the IIM ($N_Z = 2$) with BPSK and the conventional modulation for QPSK can be reached spectral efficiency of 2bps/Hz. Also, both of the IIM ($N_Z = 4$) with BPSK and 8-PSK can be reached to 3bps/Hz. However, comparing in low SNR, the spectral efficiency of the proposed system show better performance than the conventional modulation techniques QPSK and 8-PSK in the same bpcu.

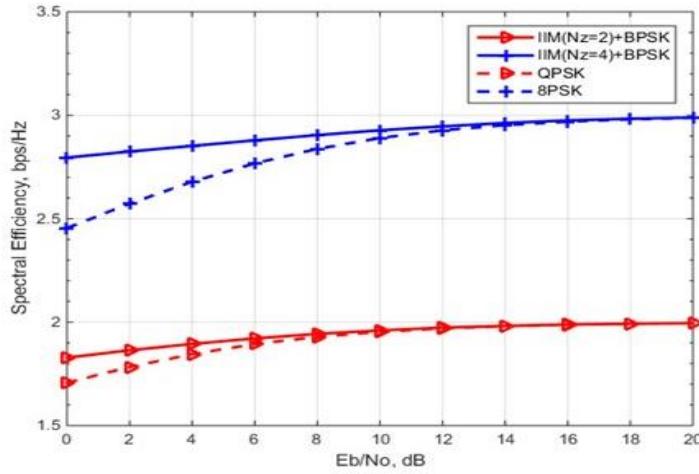


Figure 5. Spectral efficiency with different value of N_Z

In Figure 6, we present BER of our proposed system and the conventional modulation scheme according to the number of impedances. The parameter is equal to the fig.5 e.g. the number of impedance and the modulation order. As a result, When BER is 10^{-2} , it is improved by about 0.5dB in case of $N_Z = 2$,

and about 1dB in case of $N_Z = 4$ compared with conventional modulation BER of IIM performance improved compared to M-PSK modulation. In addition, compared with different impedance number in IIM, when the number of impedance is increased, BER performance decreases in the simulation result. Especially, at low SNR, BER is better in the less number of impedance, i.e., the proposed technique is strong against noise.

Figure 7 shows the result of BER of the proposed modulation scheme at 4bpcu. The case of IIM ($N_Z = 4$) with QPSK are compared with 16PSK. At the BER is 10^{-2} , the value of IIM is 17dB and 16PSK is pointed 19dB, so the performance improved.

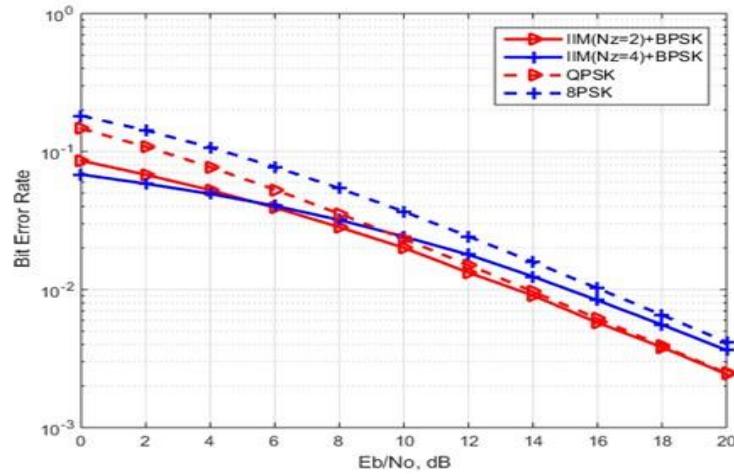


Figure 6. BER vs SNR of IIM and M-PSK

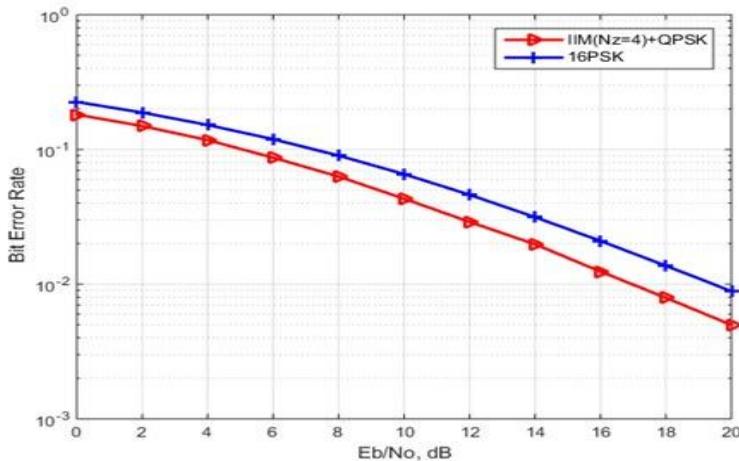


Figure 7. BER vs SNR of IIM and M-PSK in 4bpcu

5. CONCLUSION

In this paper, we suggest a novel modulation scheme using impedance matching, called IIM, to overcome explosive increasing of data traffic in 5G and beyond 5G. The key principle of the proposed system is to give indices of the result that impedance match or mismatch between transiver. To detect impedance index, we present the detecting algorithm by deriving the maximum power transfer theorem. According to the simulation result, the impedance index modulation scheme improves both the spectral efficiency and BER compared to the traditional modulation scheme. For the future, we analyze the optimal impedance value for our proposed system.

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